

IQI 04, Seminar 7

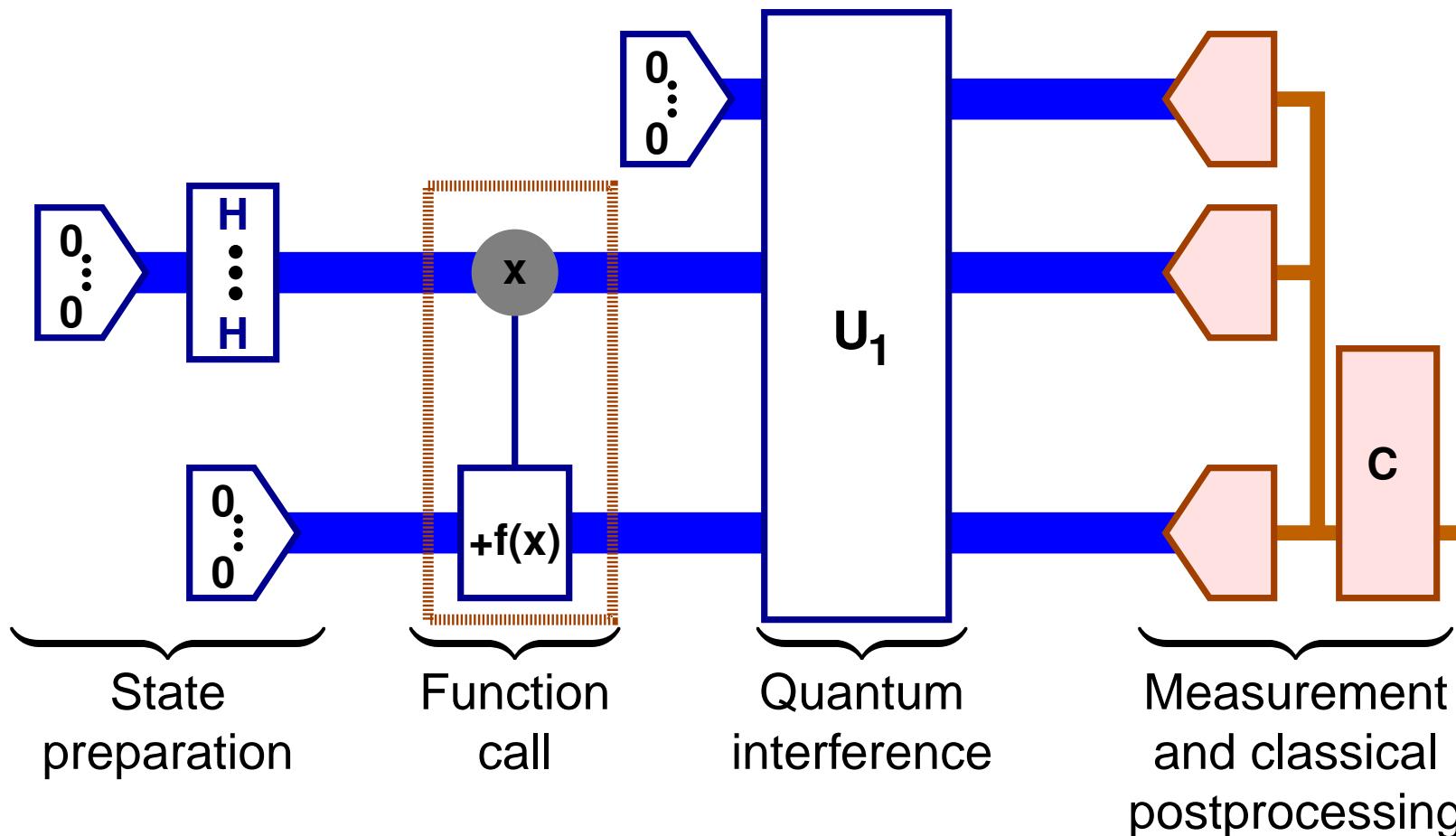
Produced with pdflatex and xfig

- Reversible classical computation.
- From irreversible to reversible computation.

E. “Manny” Knill: knill@boulder.nist.gov

Recognizing Patterns in Functions

- Algorithm structure for determining some **Property**(f).
 - One function call:



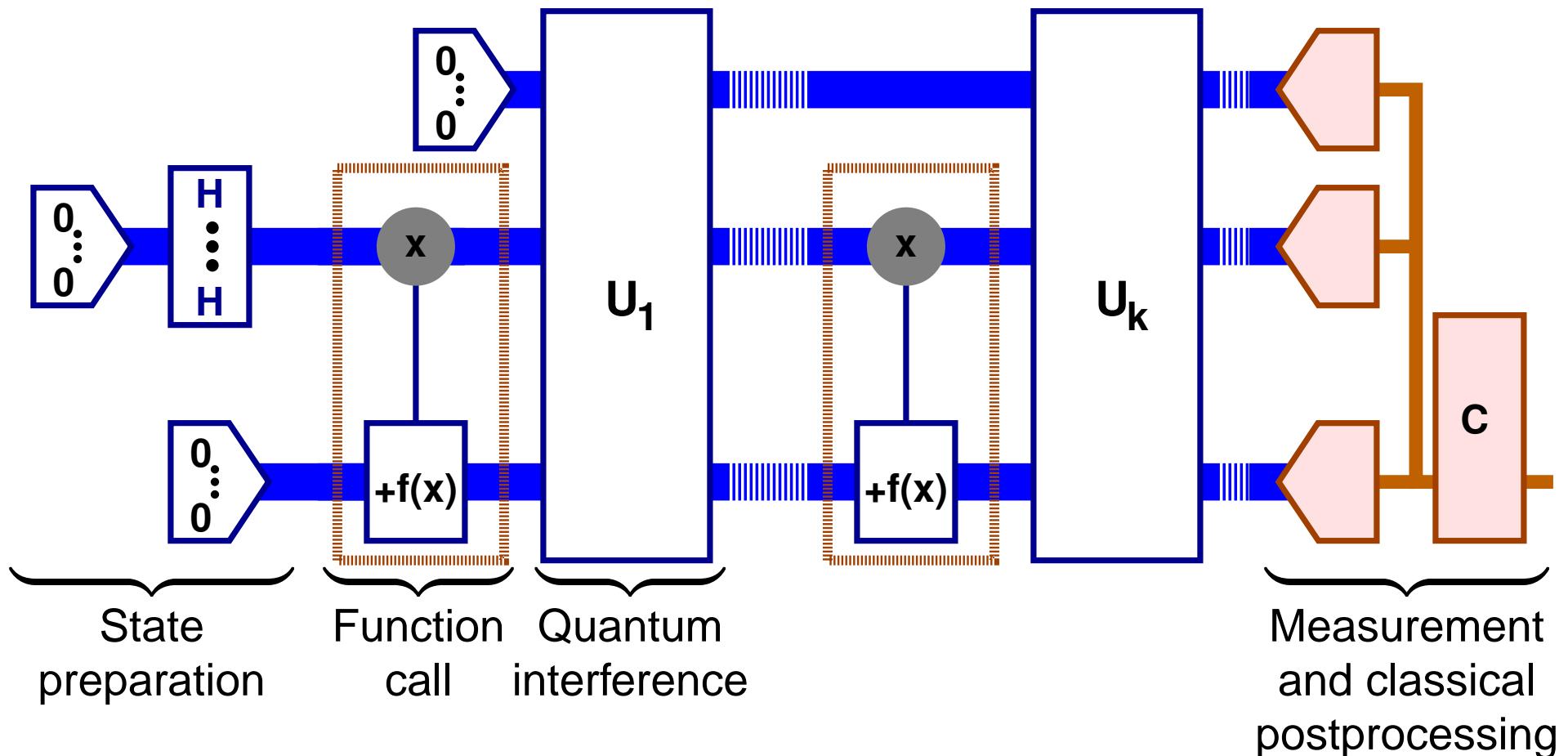
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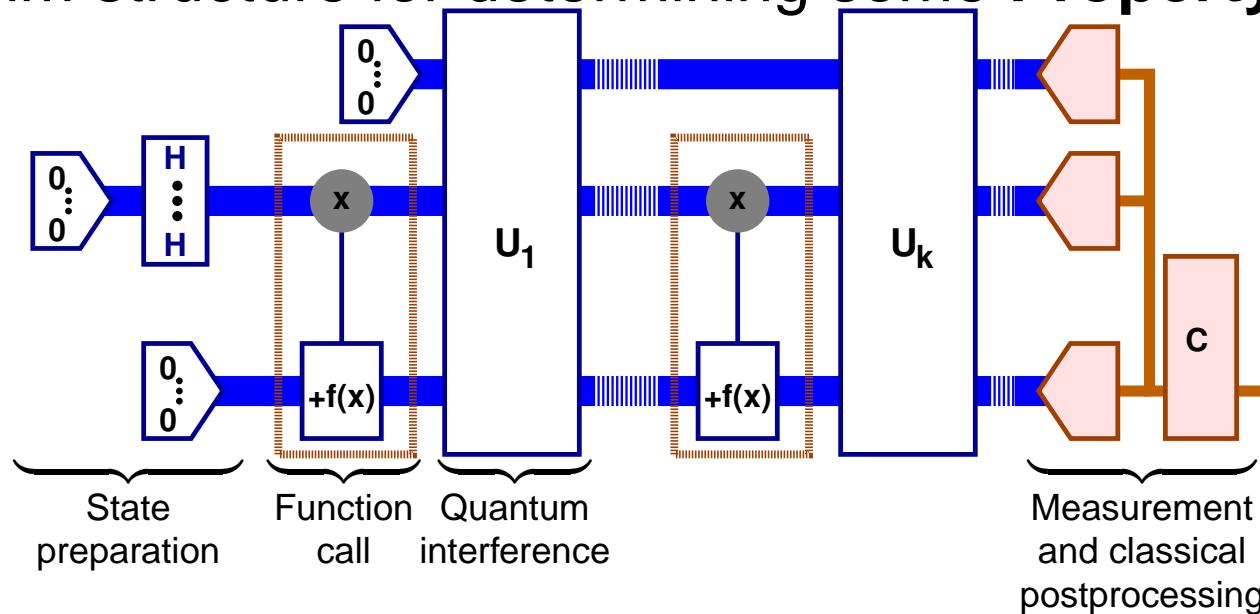
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- Algorithm structure for determining some **Property**(f).
 - Multiple function calls:



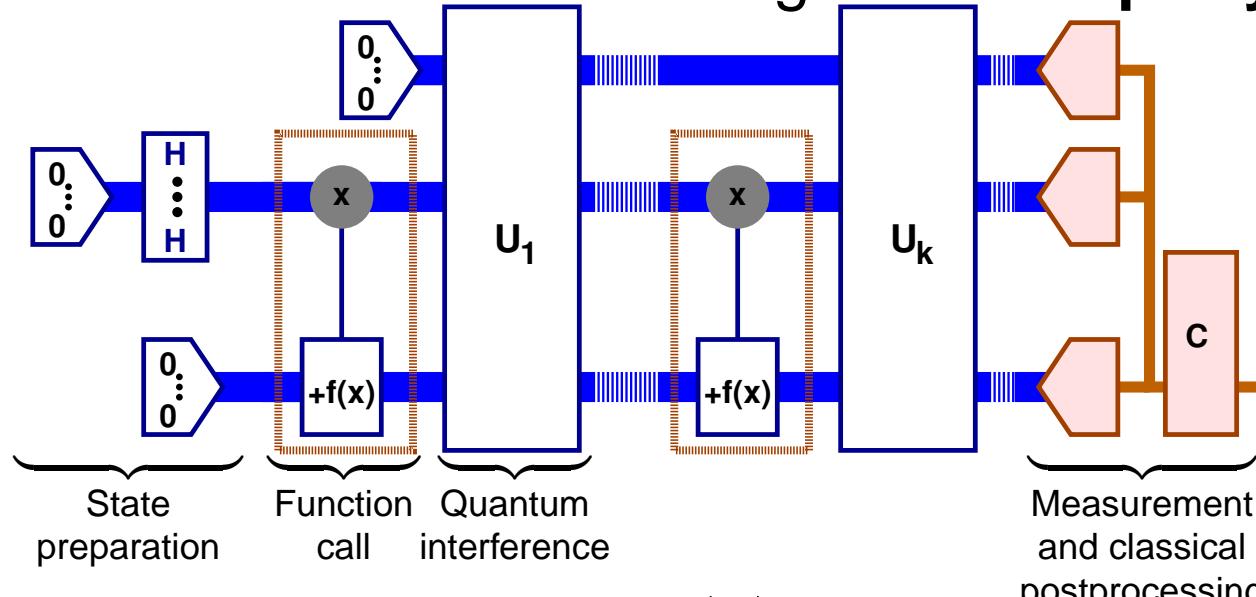
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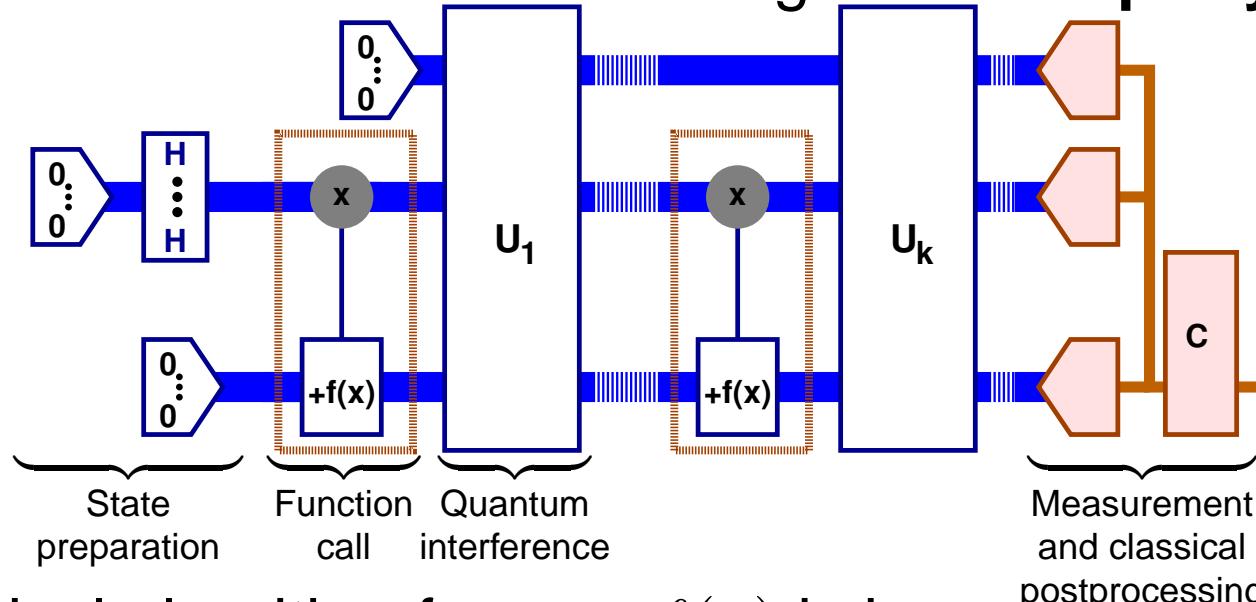
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- A classical algorithm for $x \mapsto f(x)$ is known.
 - How to implement $\sum_x \alpha_x |xy\rangle \rightarrow \sum_x \alpha_x |x(y \oplus f(x))\rangle$?

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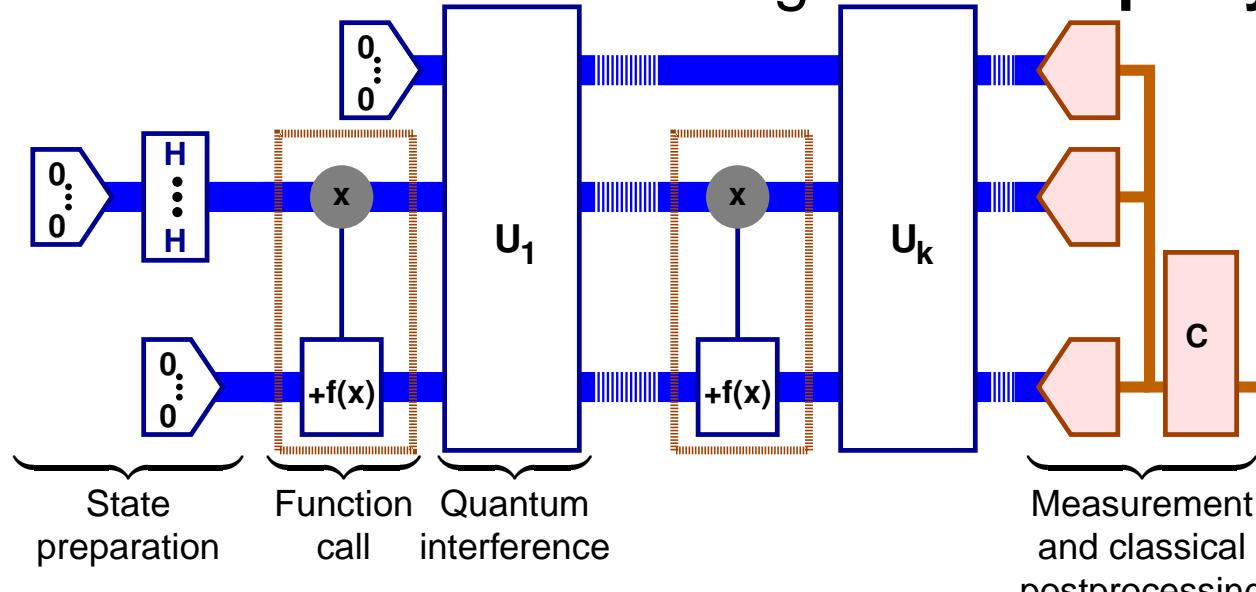
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 - Algorithm \rightarrow circuit($|x|$).

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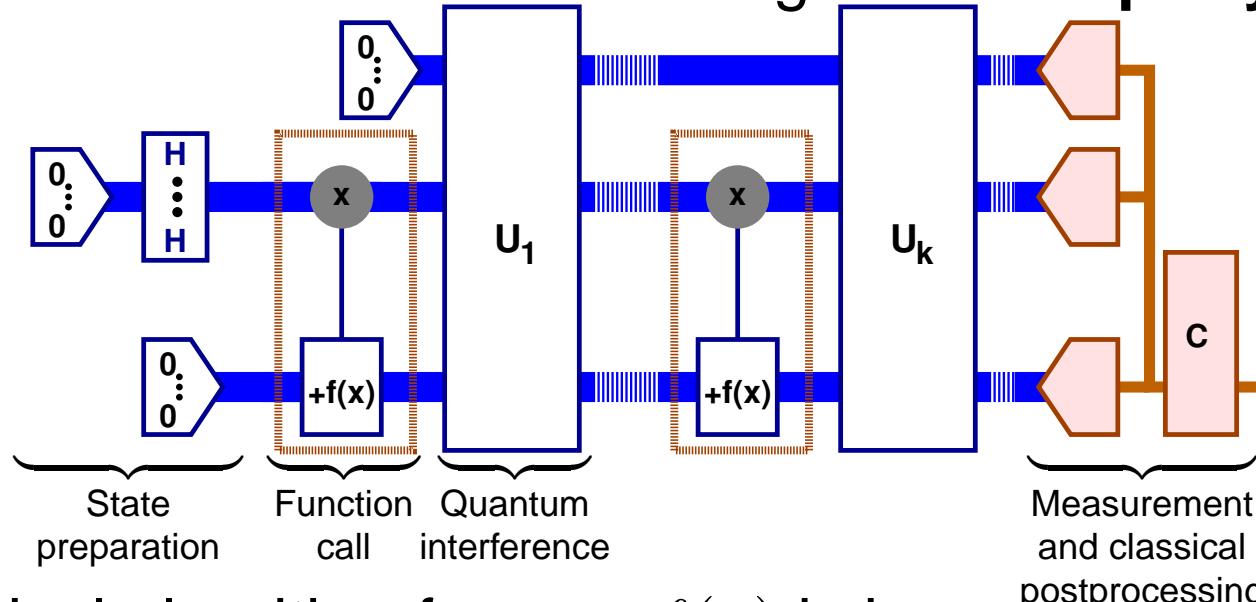
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 - Irreversible gates \rightarrow reversible gates and memory.

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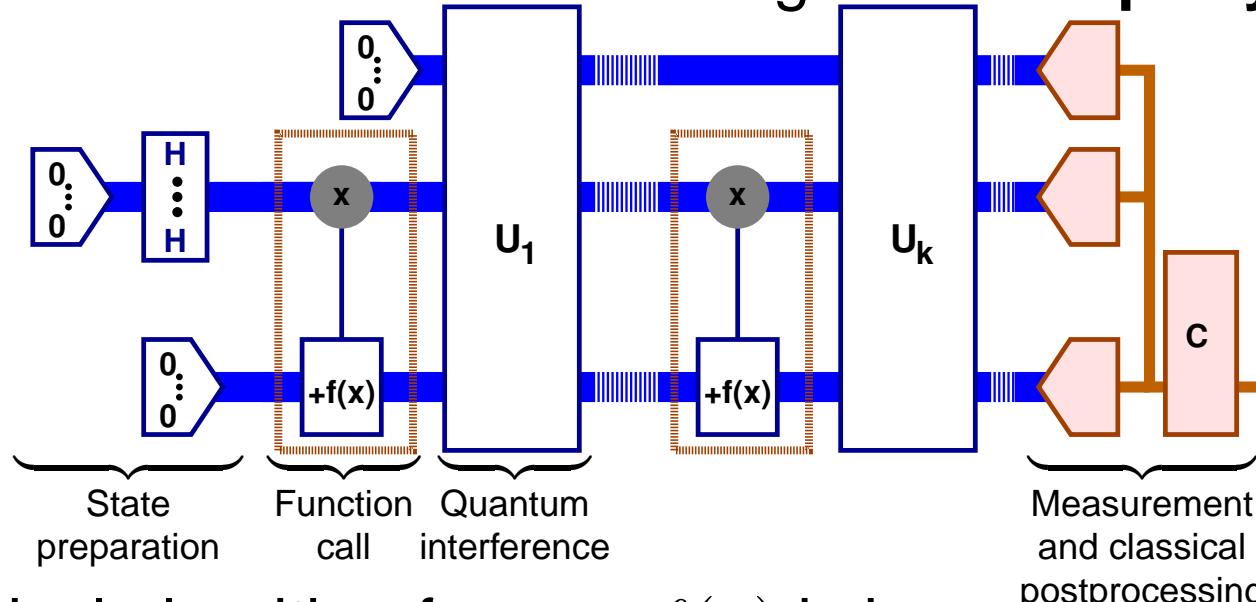
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 - Reversibly erase memory.

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- Solution:
 - Algorithm \rightarrow circuit($|x|$).
 - Irreversible gates \rightarrow reversible gates and memory.
 - Reversibly erase memory.
 - Bits \rightarrow qubits. Reversible gates \rightarrow unitary gates.

Example: Number Comparison

- Algorithm for comparing two binary numbers.

$\text{COMP}(x, y)$

Input: Zero-filled n -bit numbers $x = x_{n-1} \dots x_0$ and $y = y_{n-1} \dots y_0$.

Output: 0 if $x < y$ and 1 if $x \geq y$.

```
k ← n − 1
c ← −1
while k ≥ 0 & c < 0
    if  $x_k < y_k$ 
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	c	:
x	: 0 1 0 1 0 1	
y	: 0 1 0 0 1 1	



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k	:	5
c	:	
x	:	0 1 0 1 0 1
y	:	0 1 0 0 1 1



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k :	4					
c :	-1					
x :	0	1	0	1	0	1
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k :	3					
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k :	2
c :	-1
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   $\Rightarrow \ k \leftarrow k - 1$   
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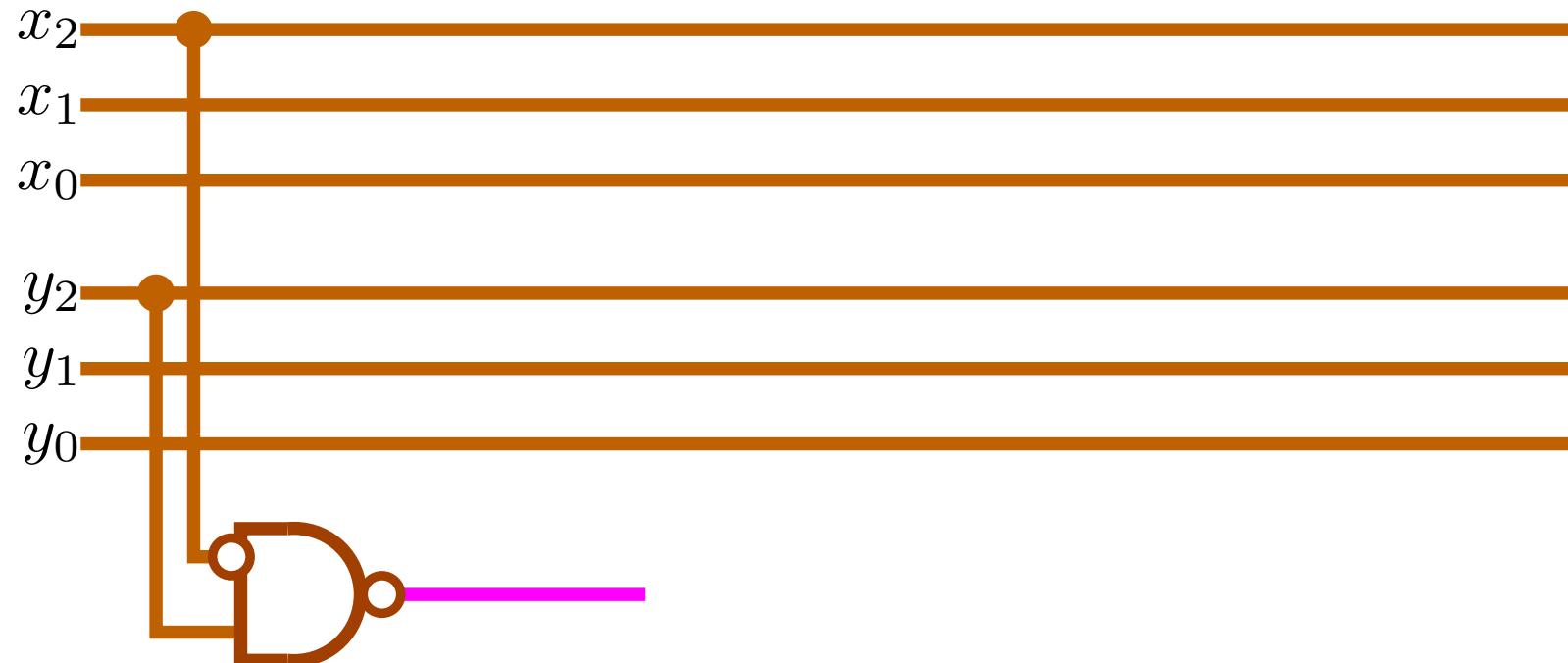
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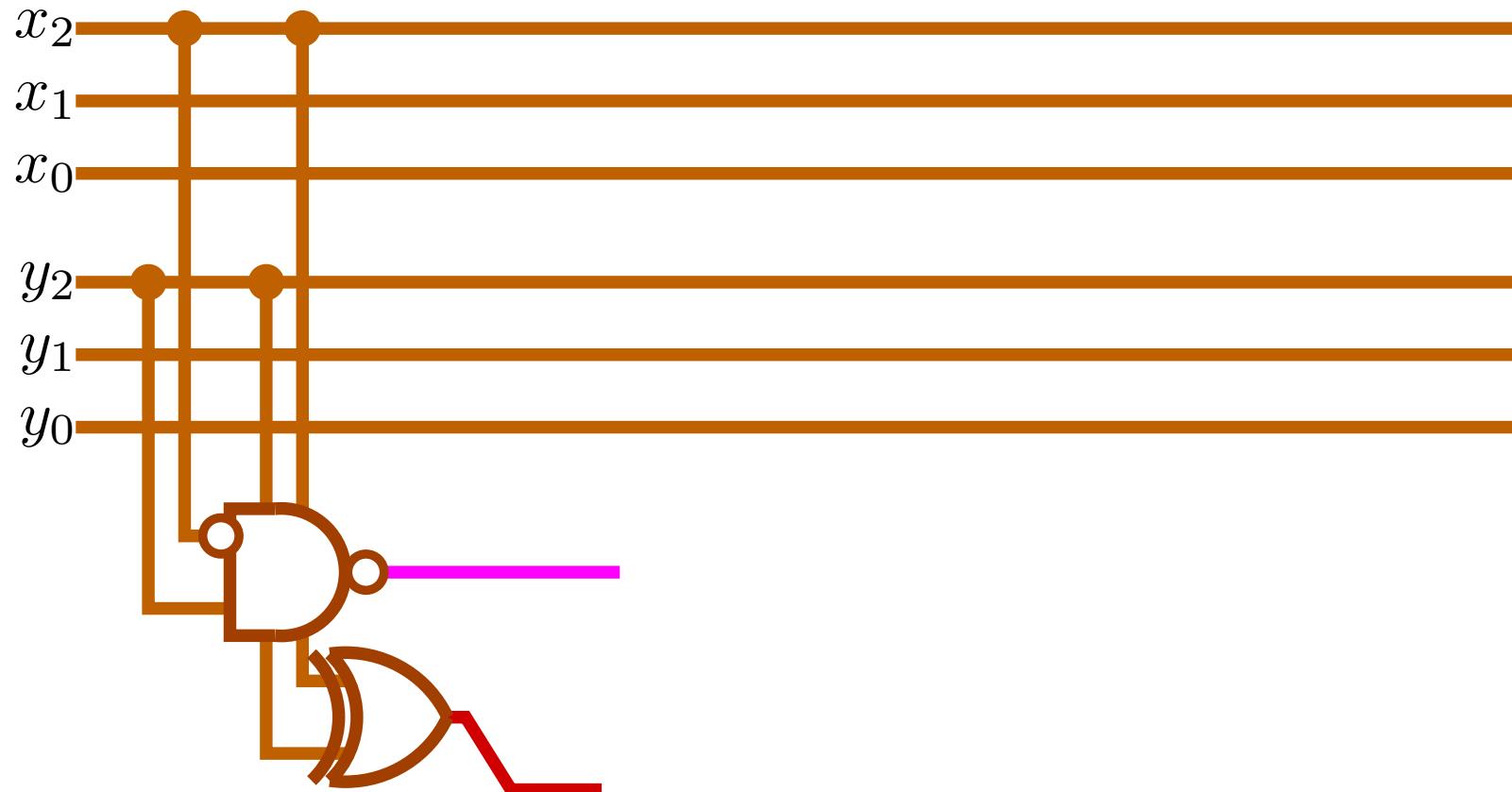
Classical Comparison Circuit

- 3 bit comparison circuit for “if $x < y$ then 0 else 1”.



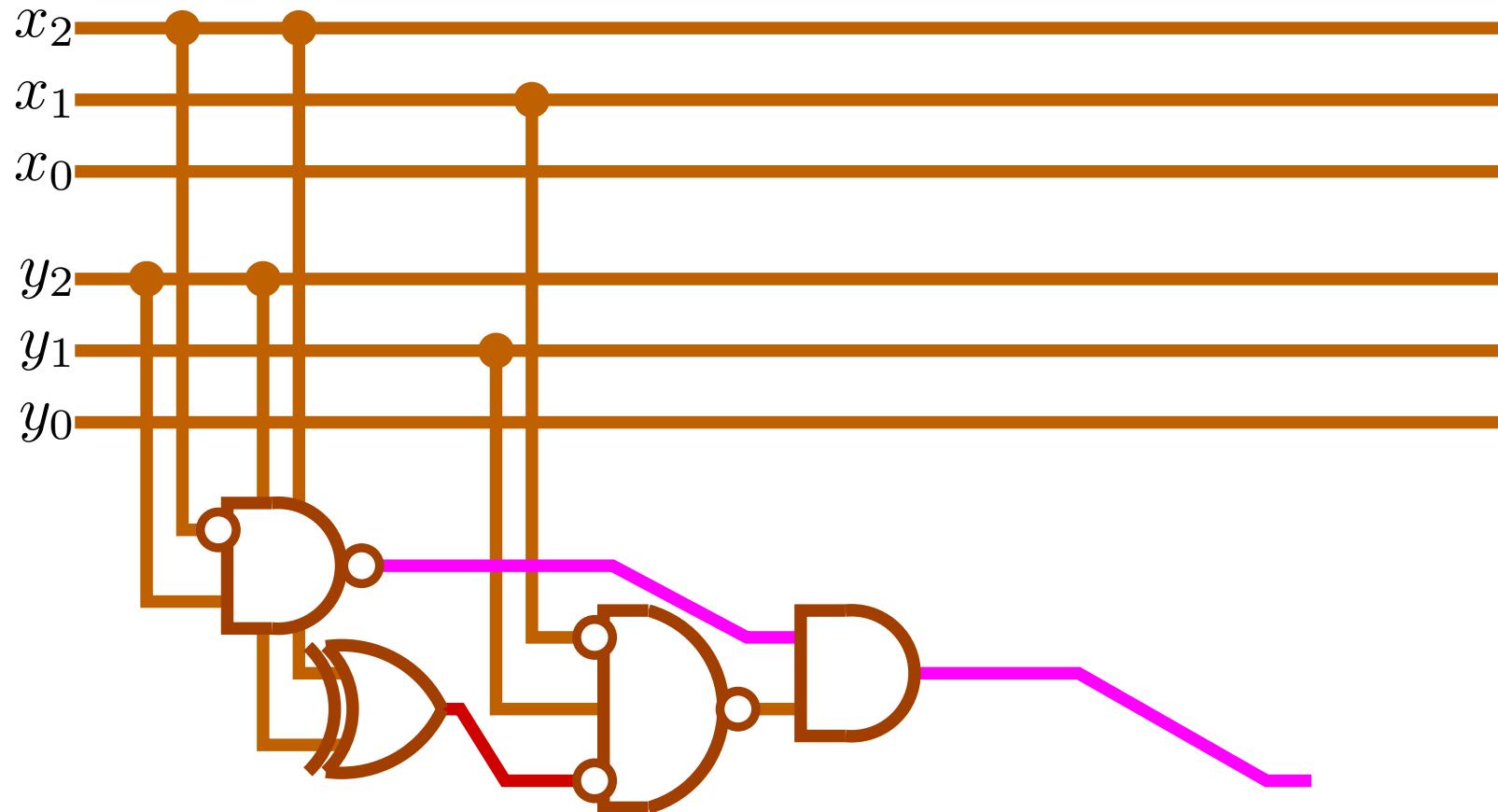
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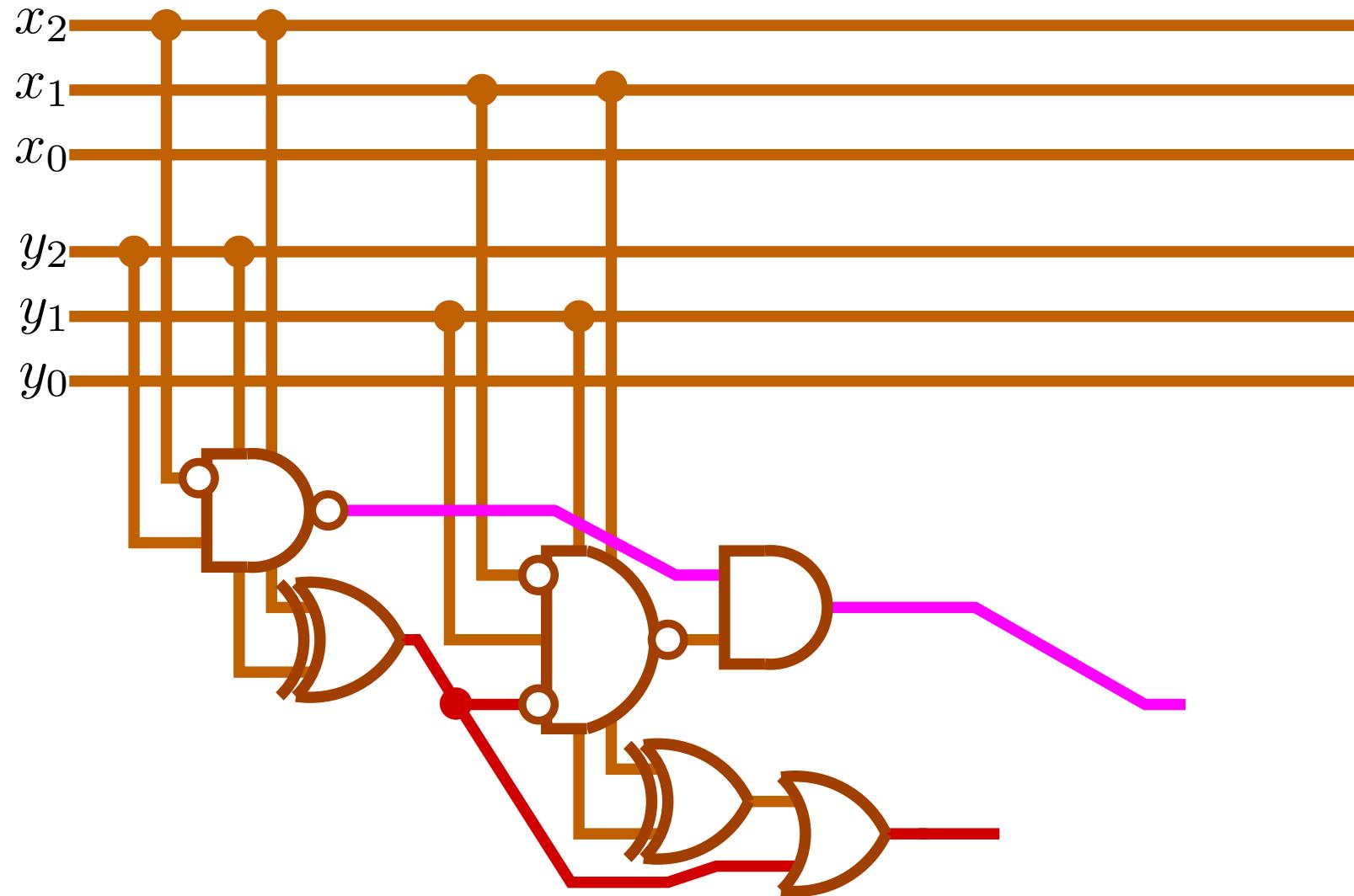
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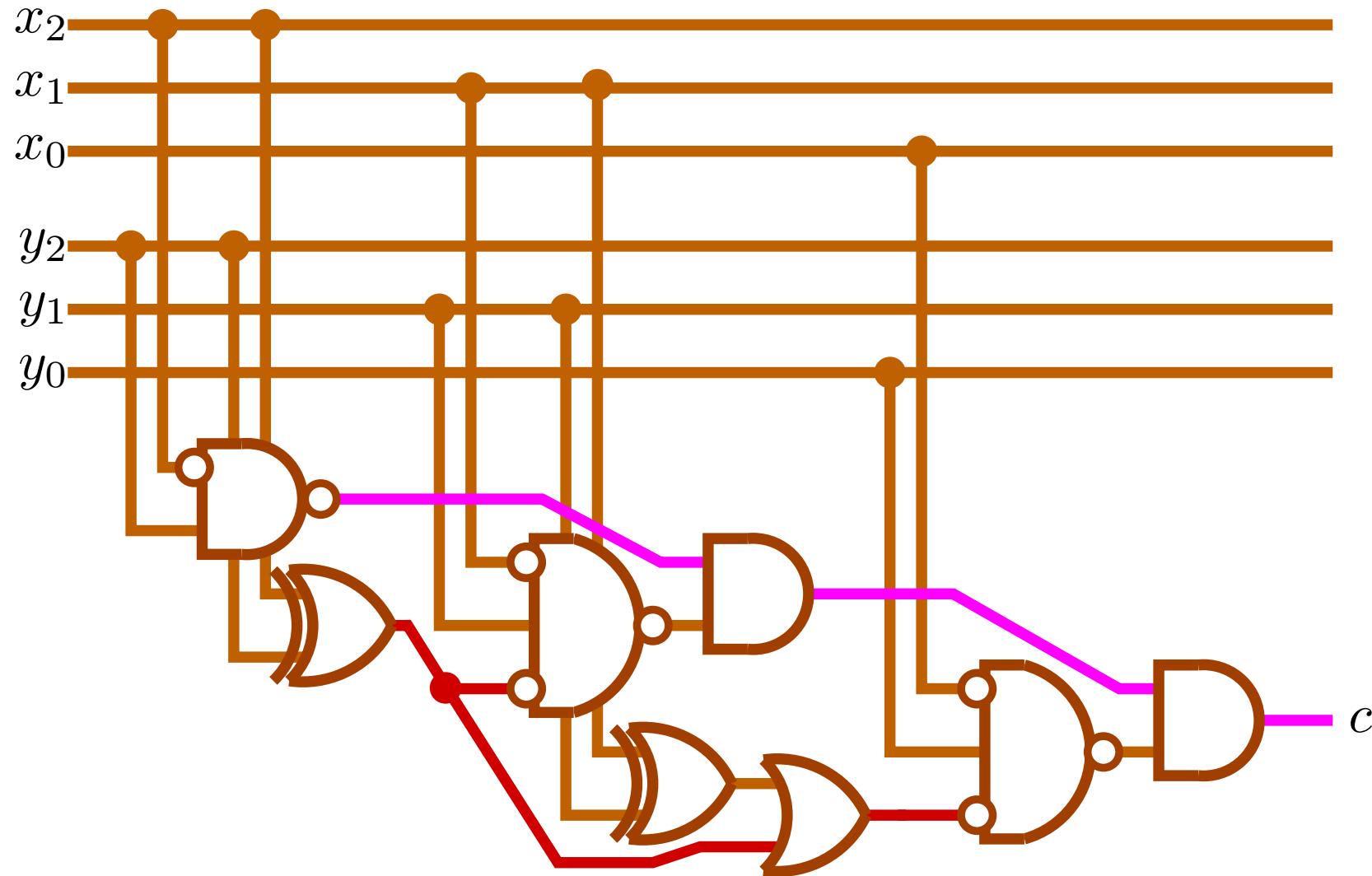
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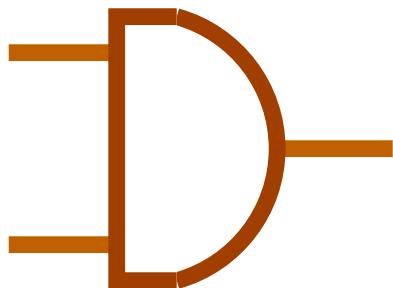
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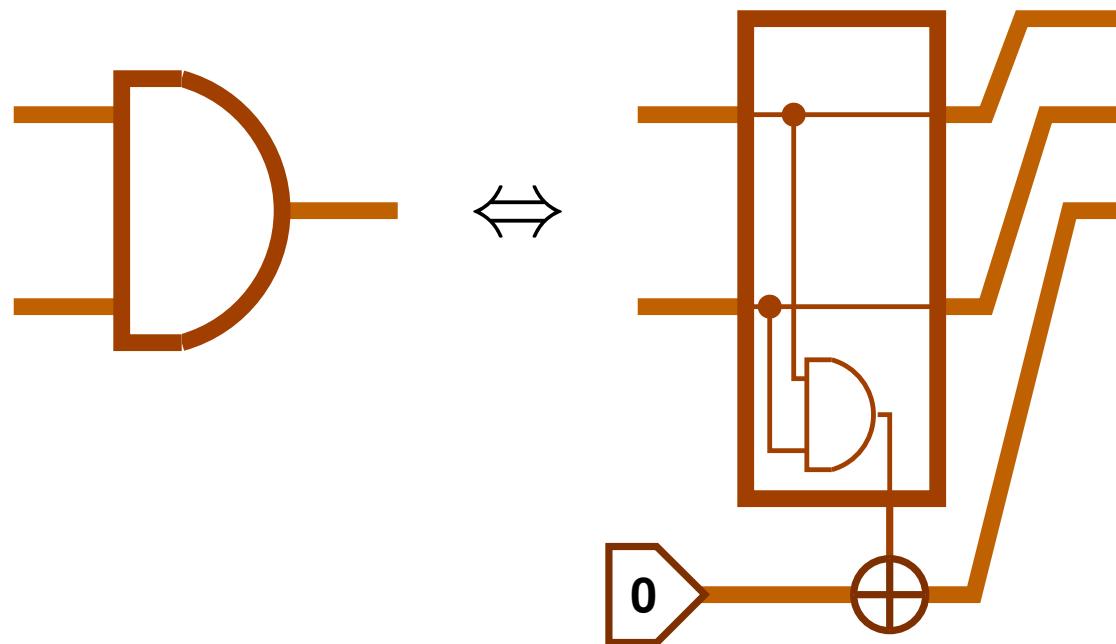
Converting to Reversible Logic

- Reversifying the and gate.



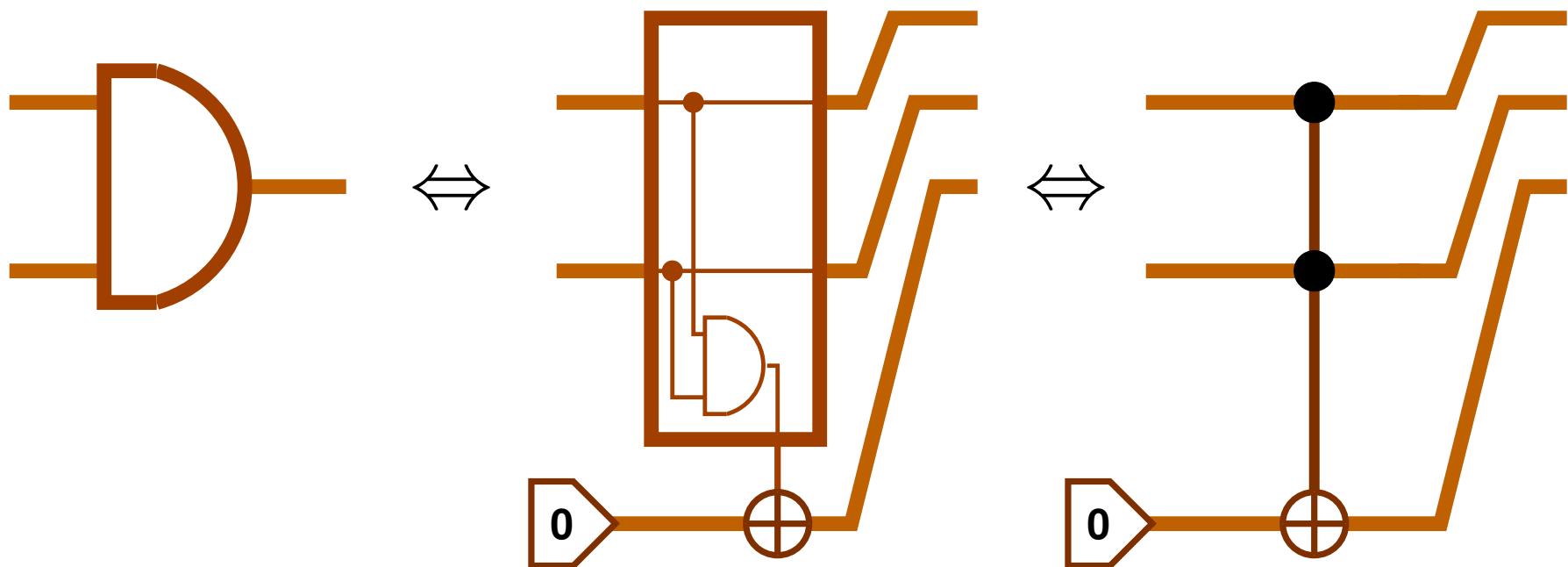
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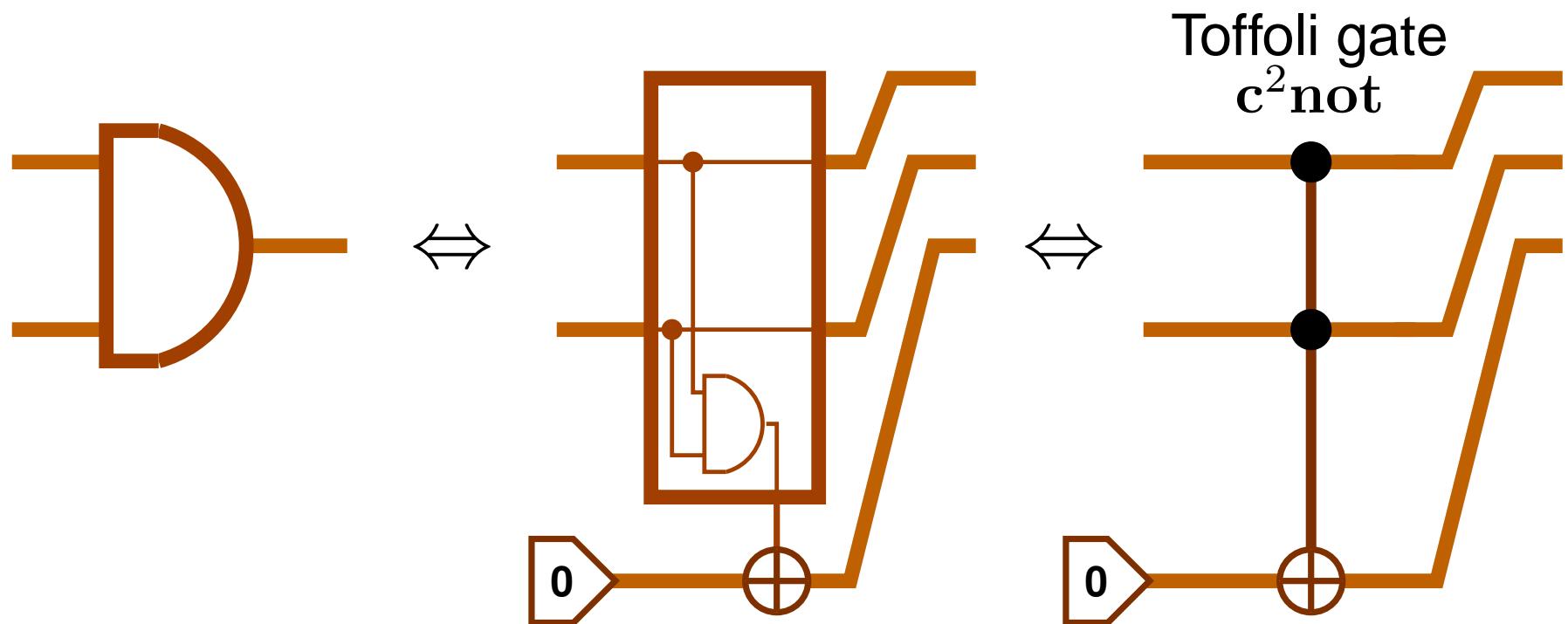
Converting to Reversible Logic

- Reversifying the and gate.



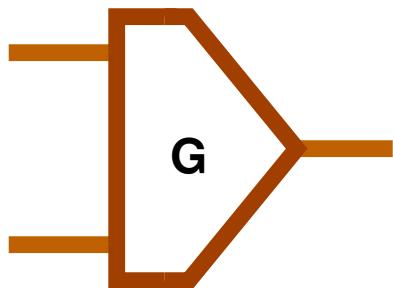
Converting to Reversible Logic

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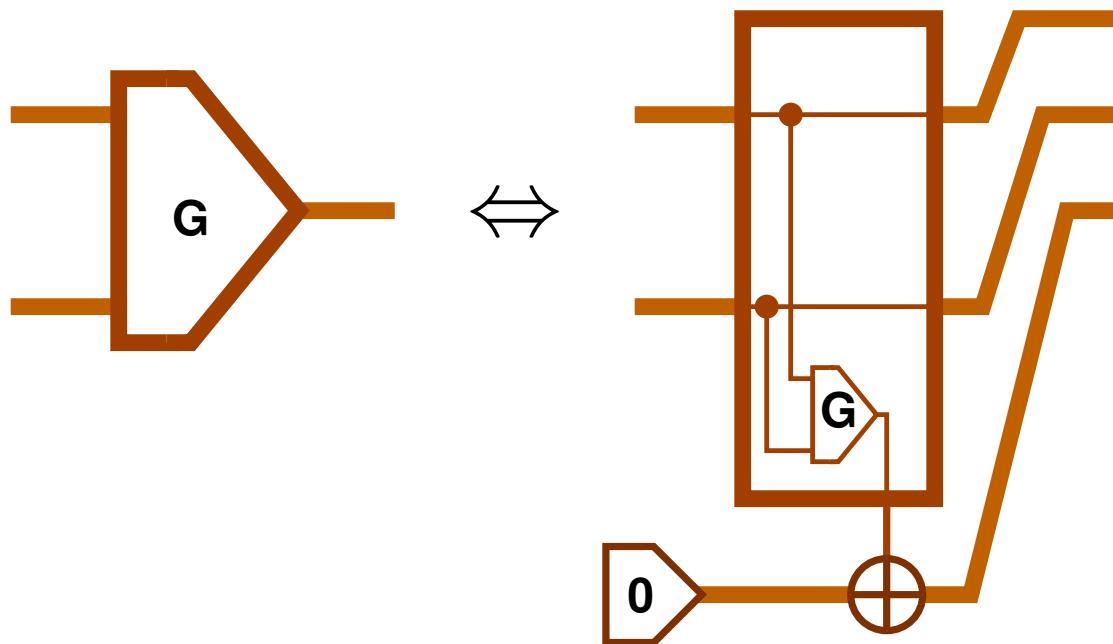
Converting to Reversible Logic

- Reversifying a general gate.



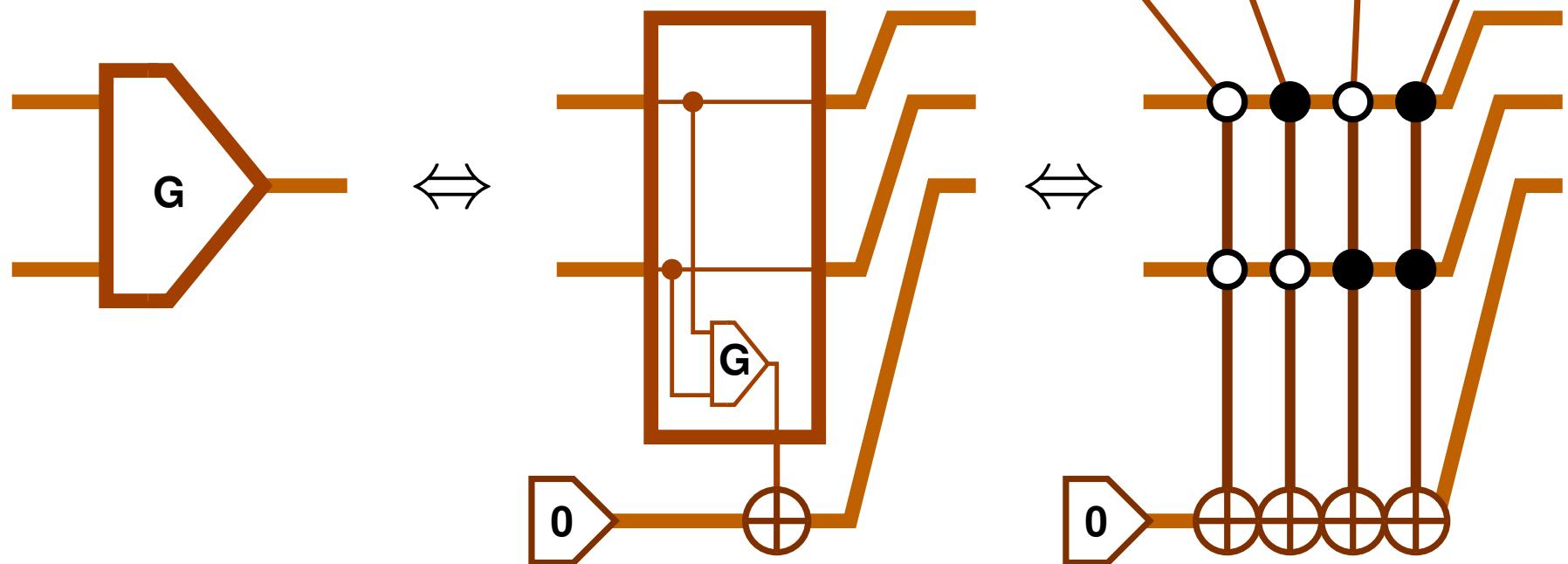
Converting to Reversible Logic

- Reversifying a general gate.



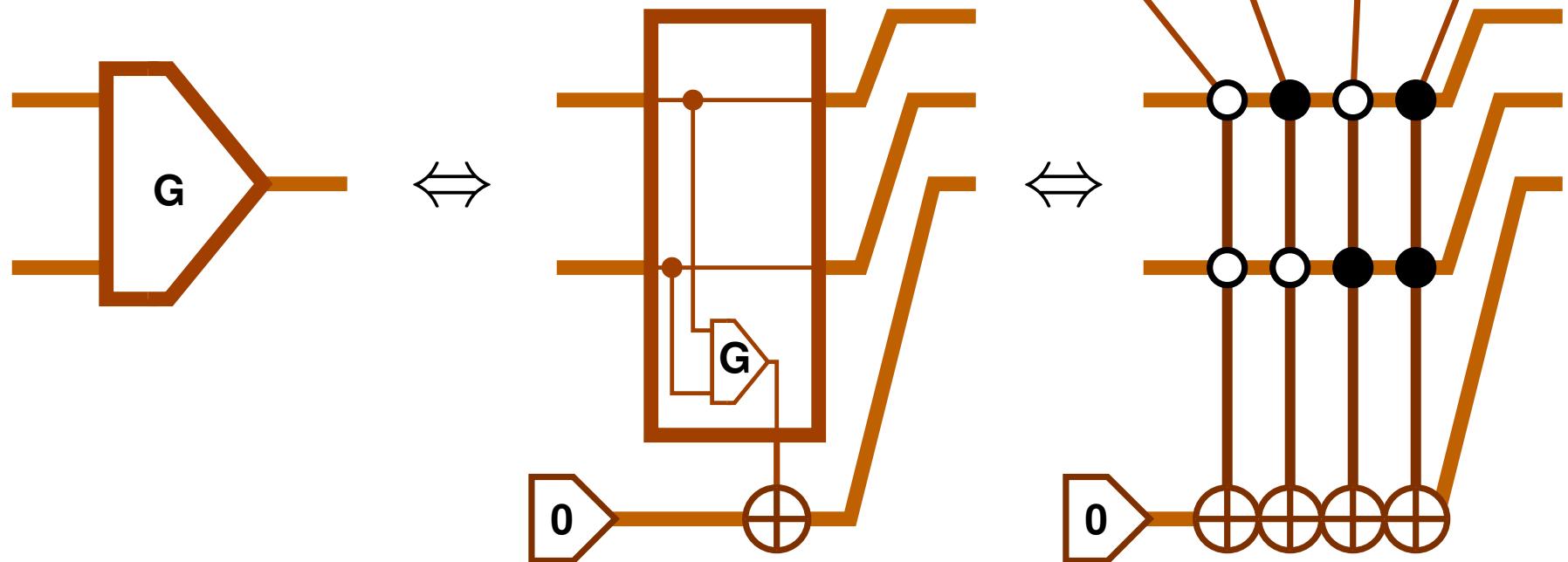
Converting to Reversible Logic

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Converting to Reversible Logic

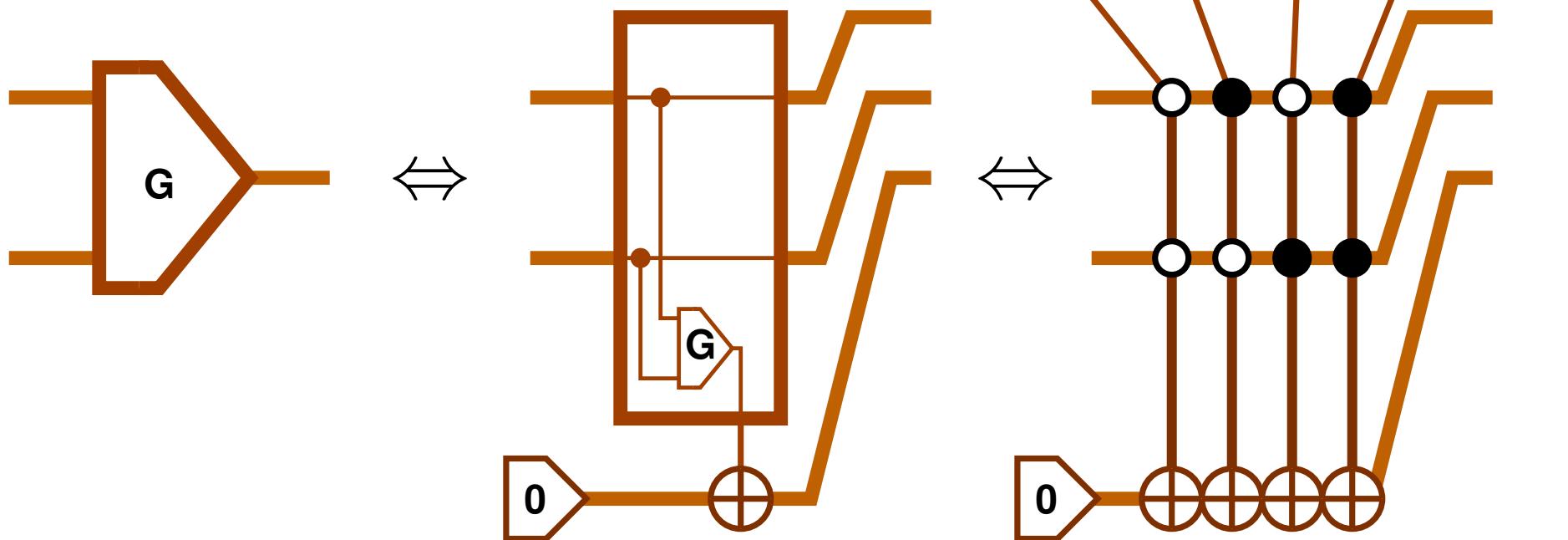
- Reversifying a general gate.



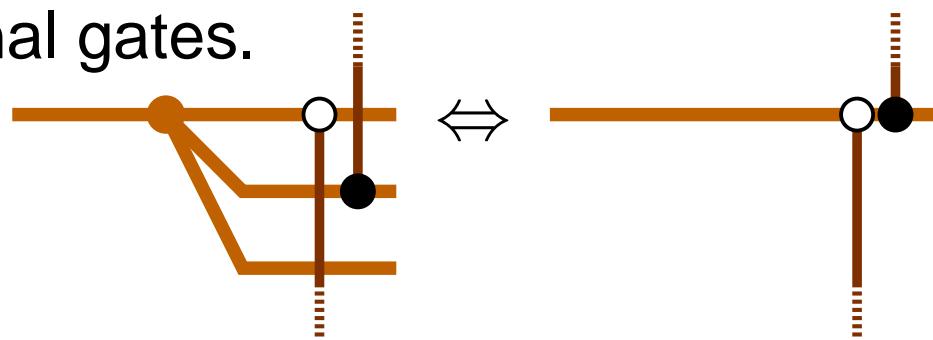
- ...then optimize the conditional gates.

Converting to Reversible Logic

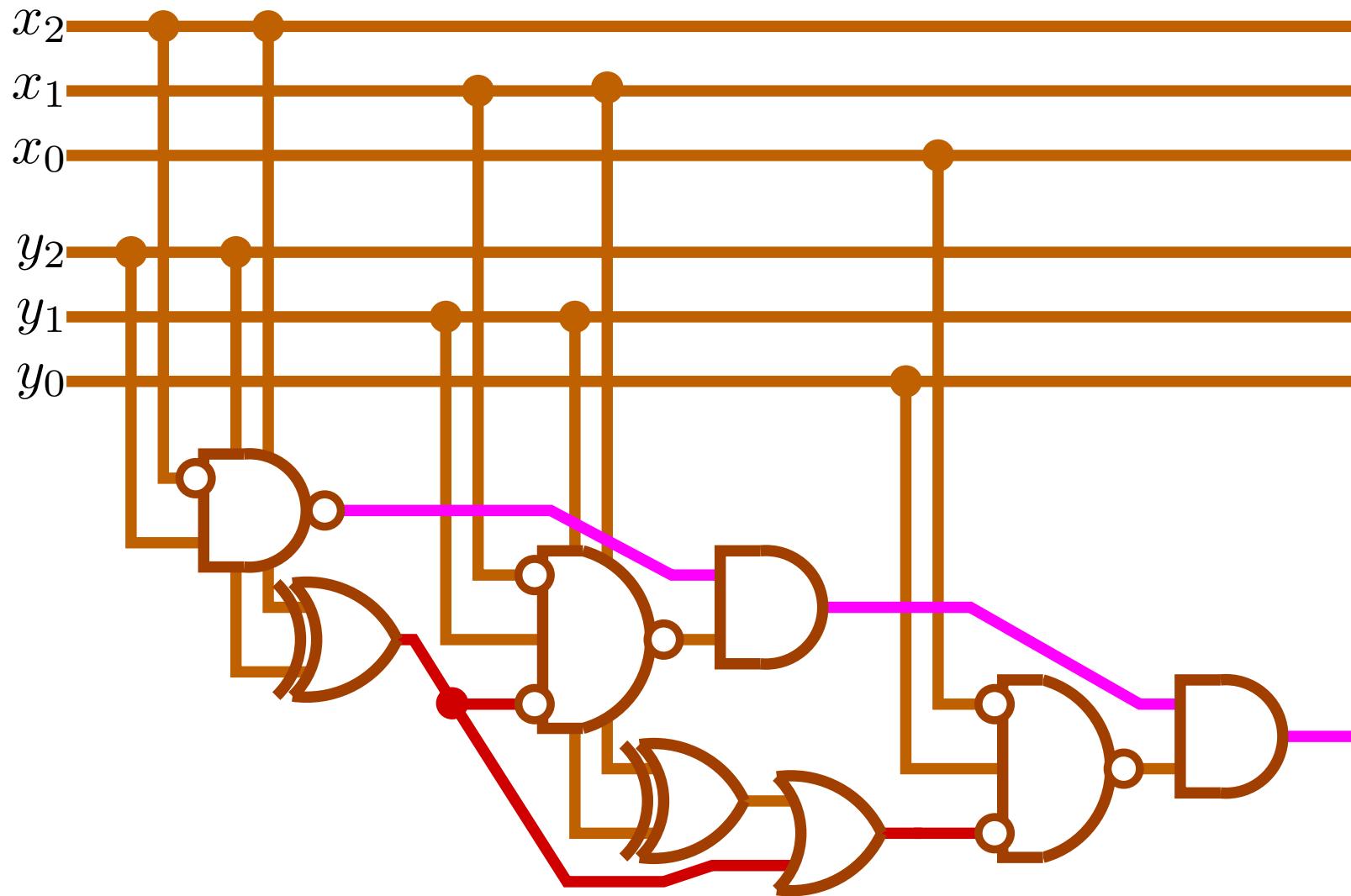
- Reversifying a general gate.



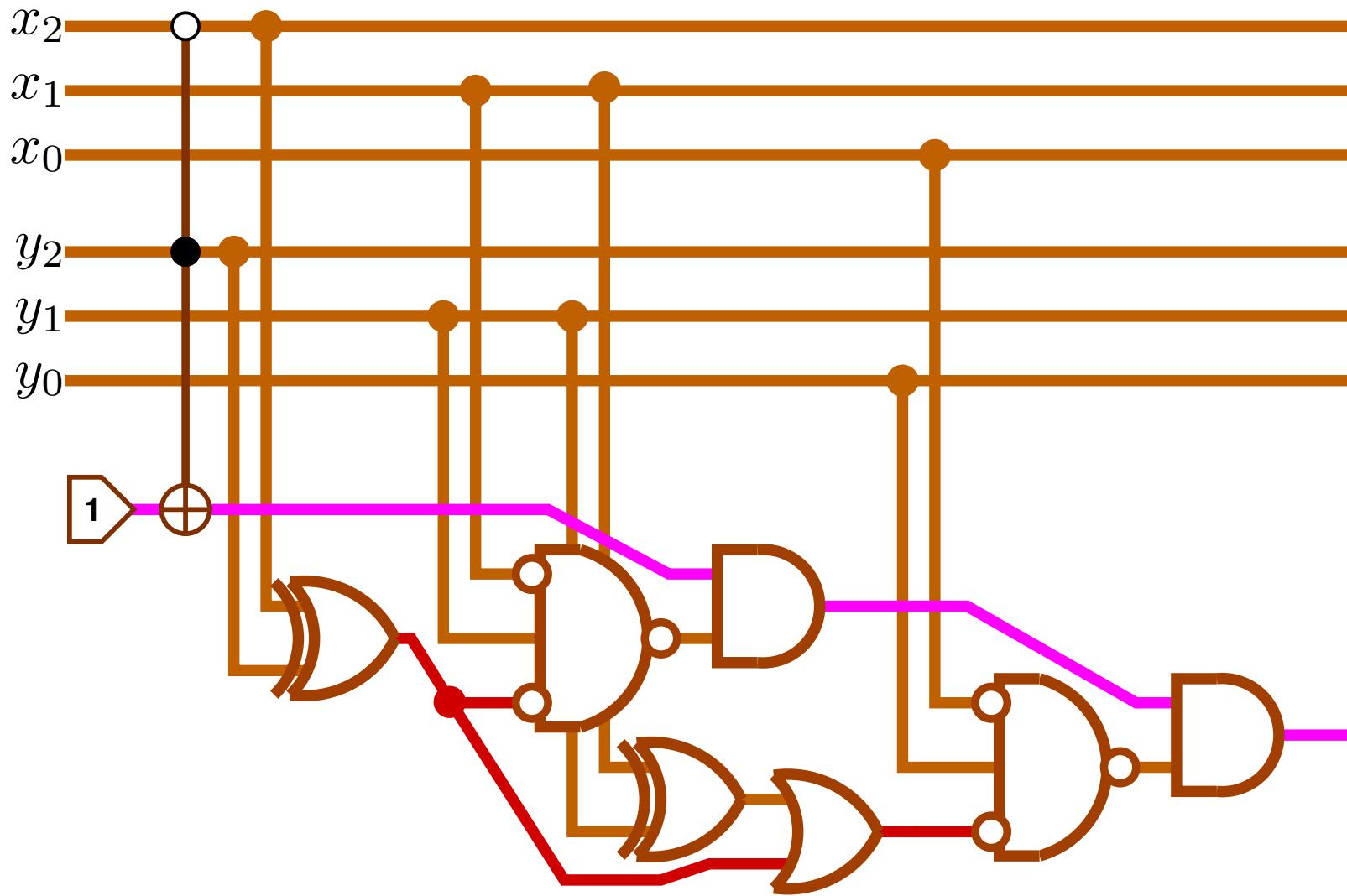
- ...then optimize the conditional gates.
- Remove redundant fanout.



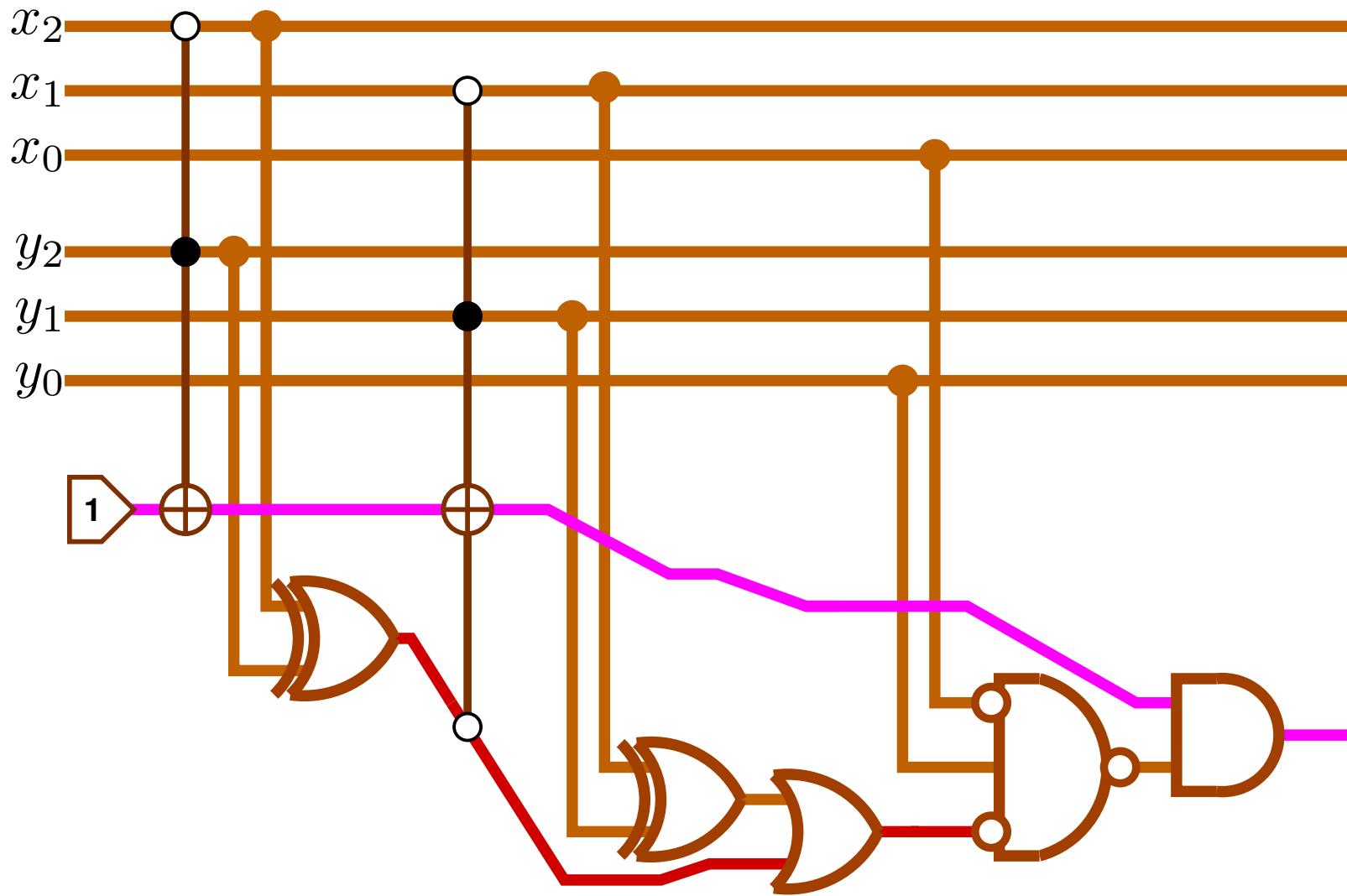
Reversifying the Comparison Circuit



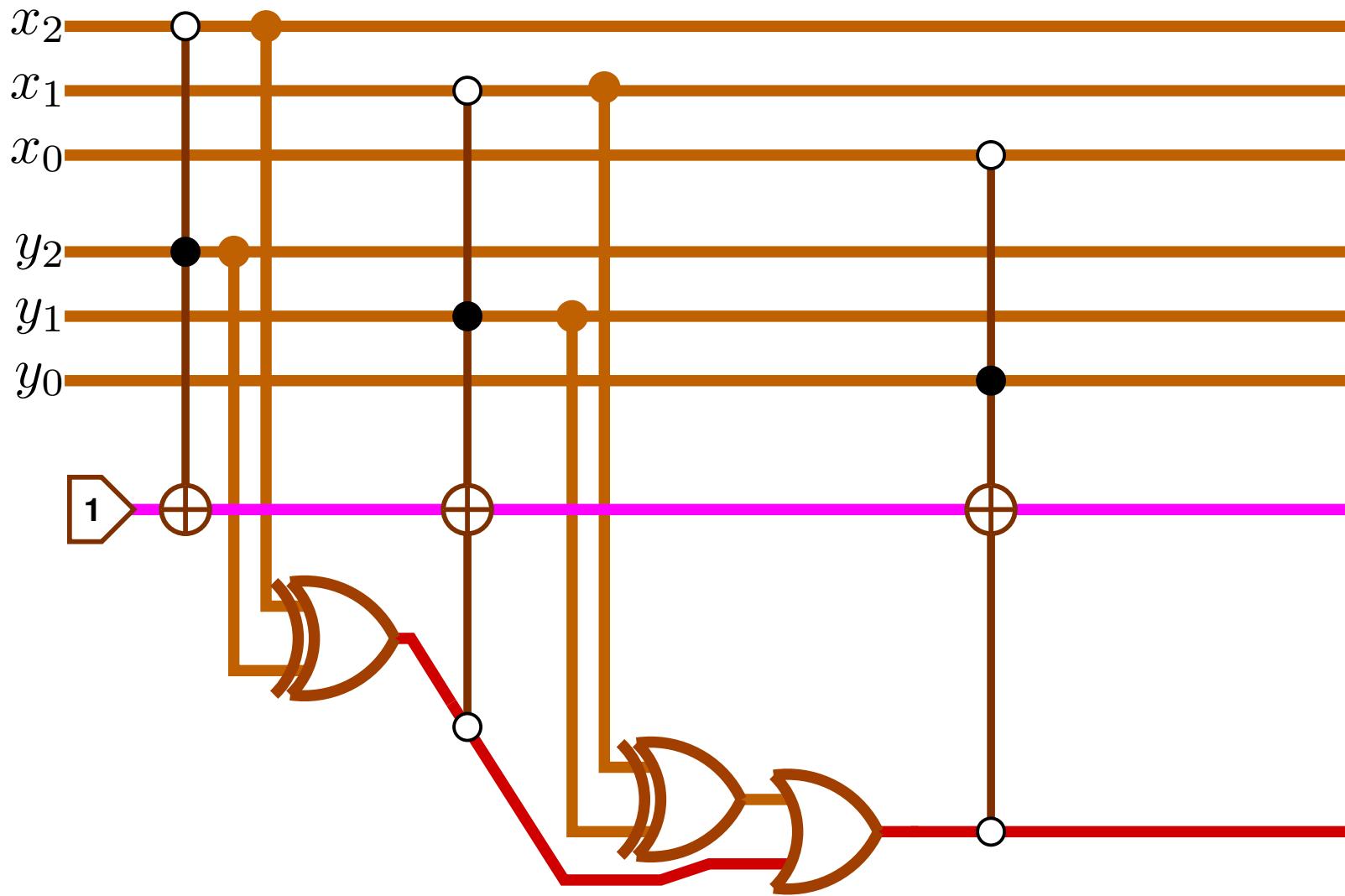
Reversifying the Comparison Circuit



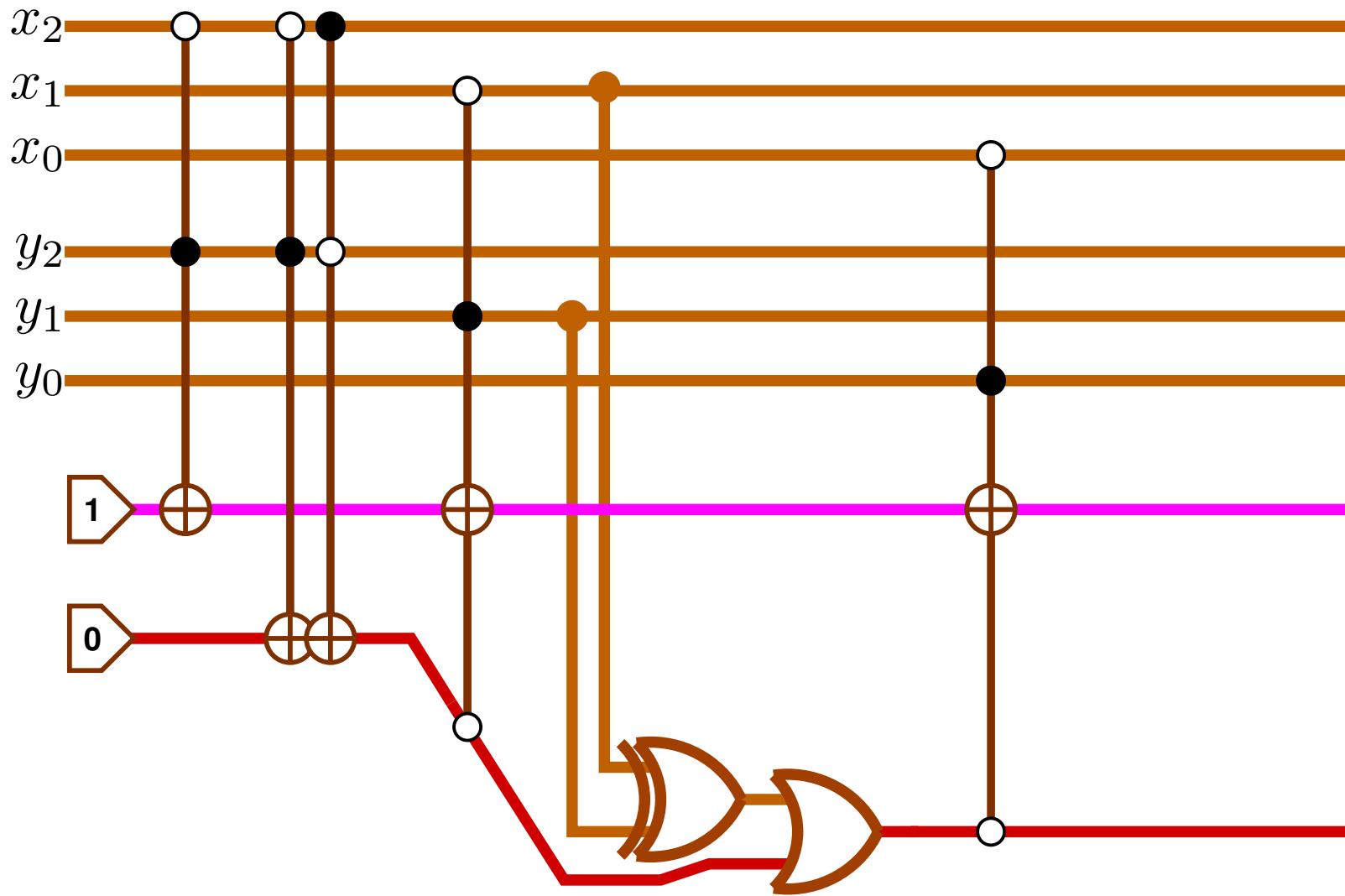
Reversifying the Comparison Circuit



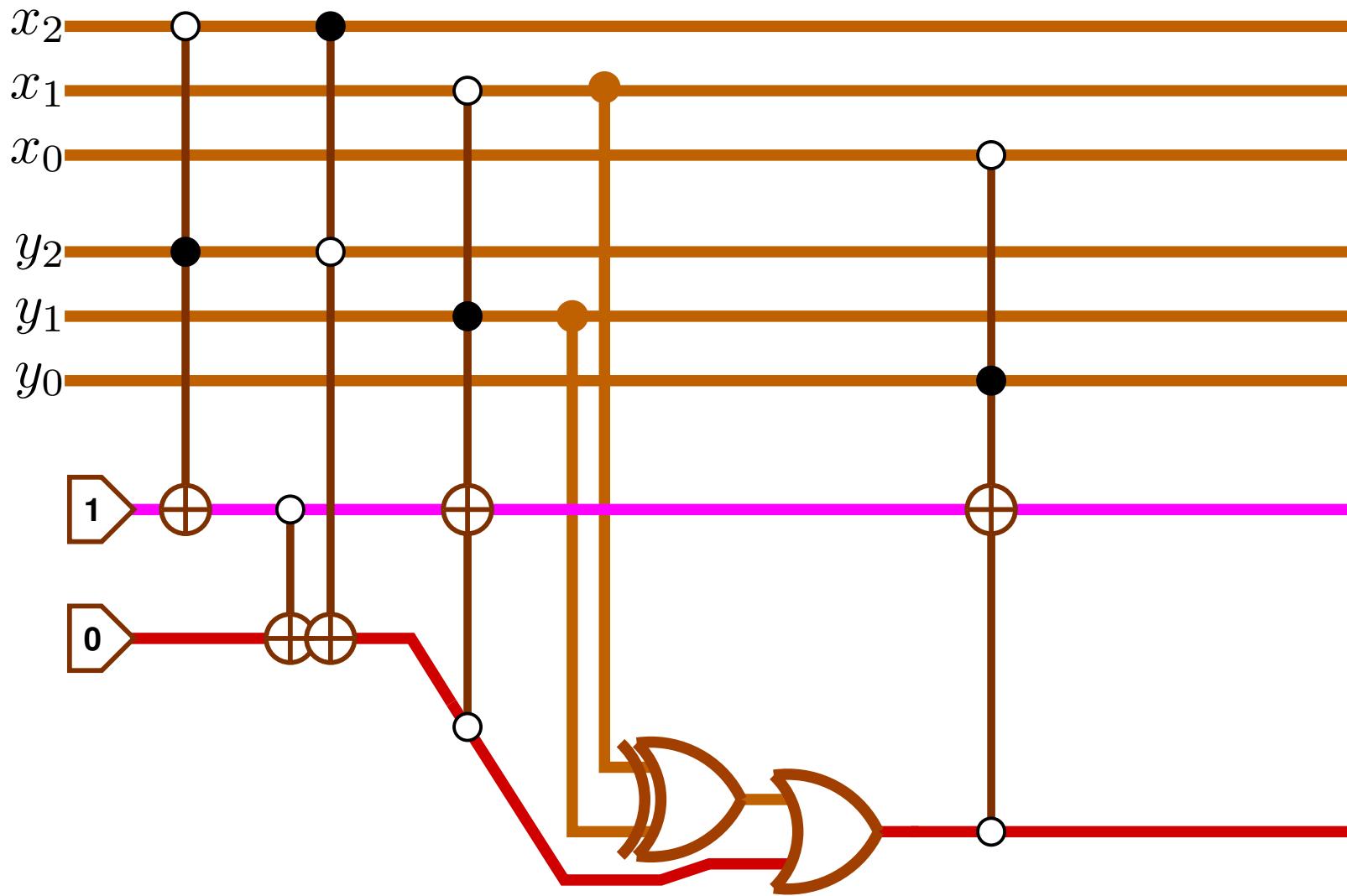
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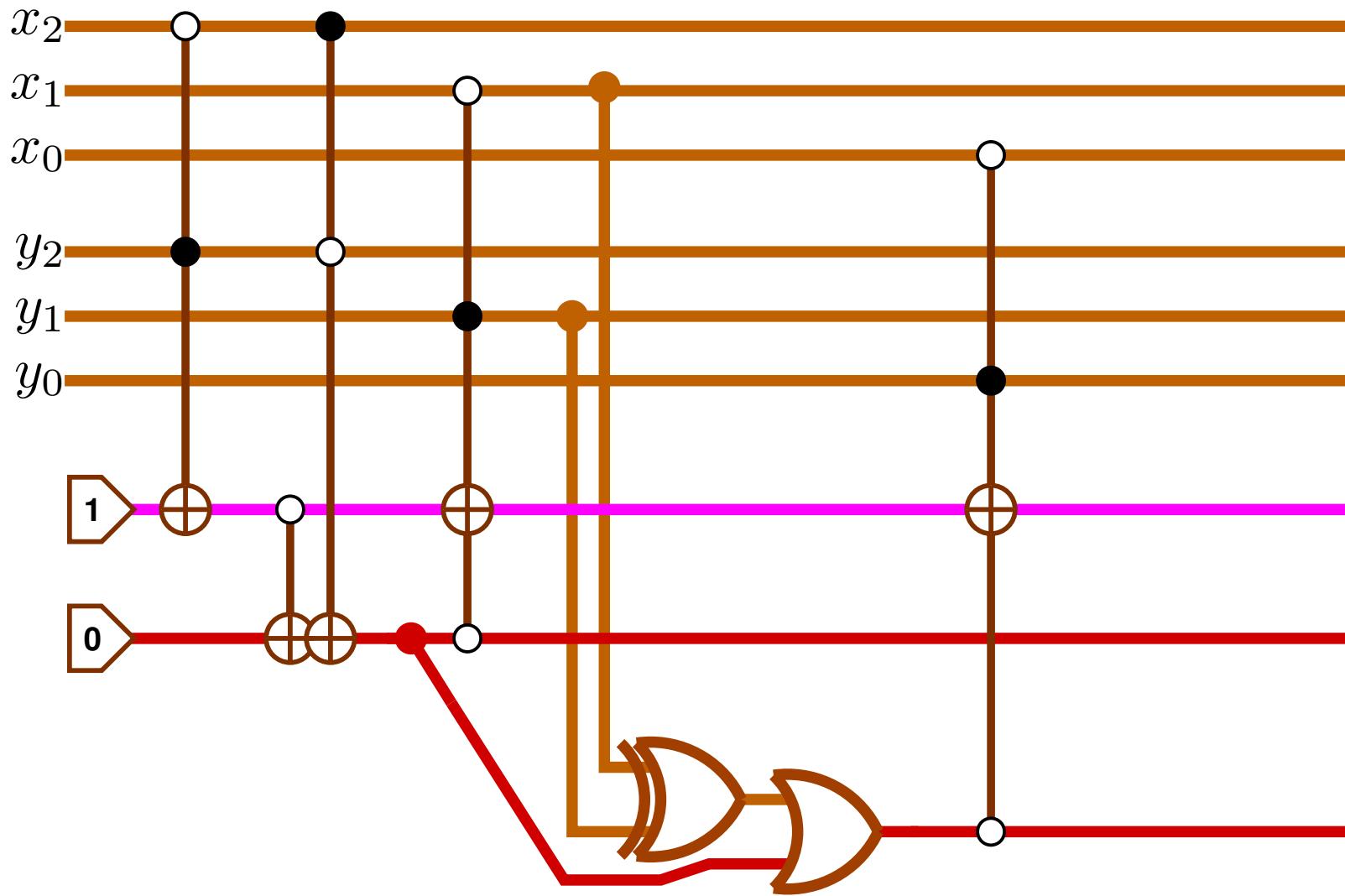
Reversifying the Comparison Circuit



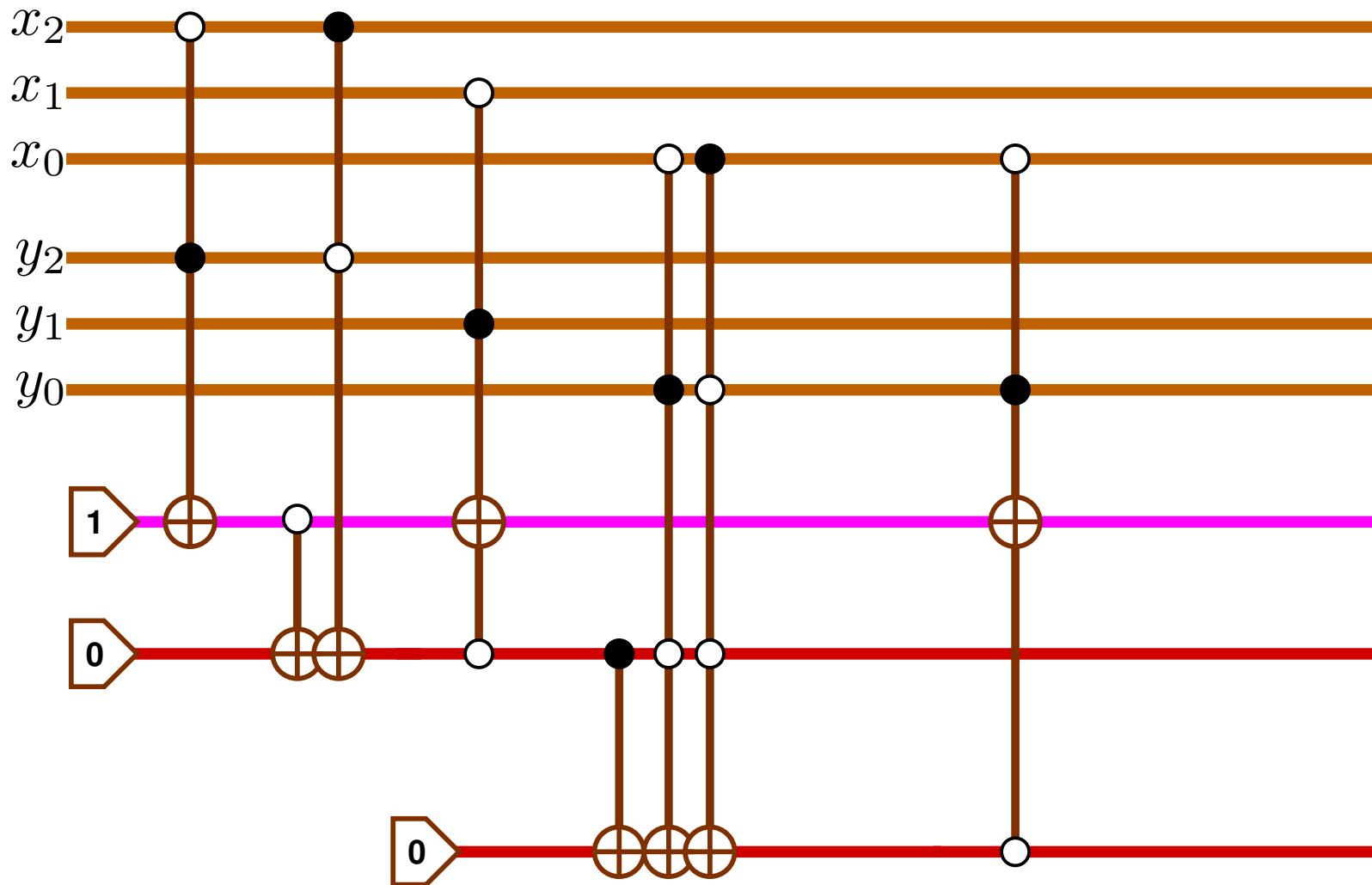
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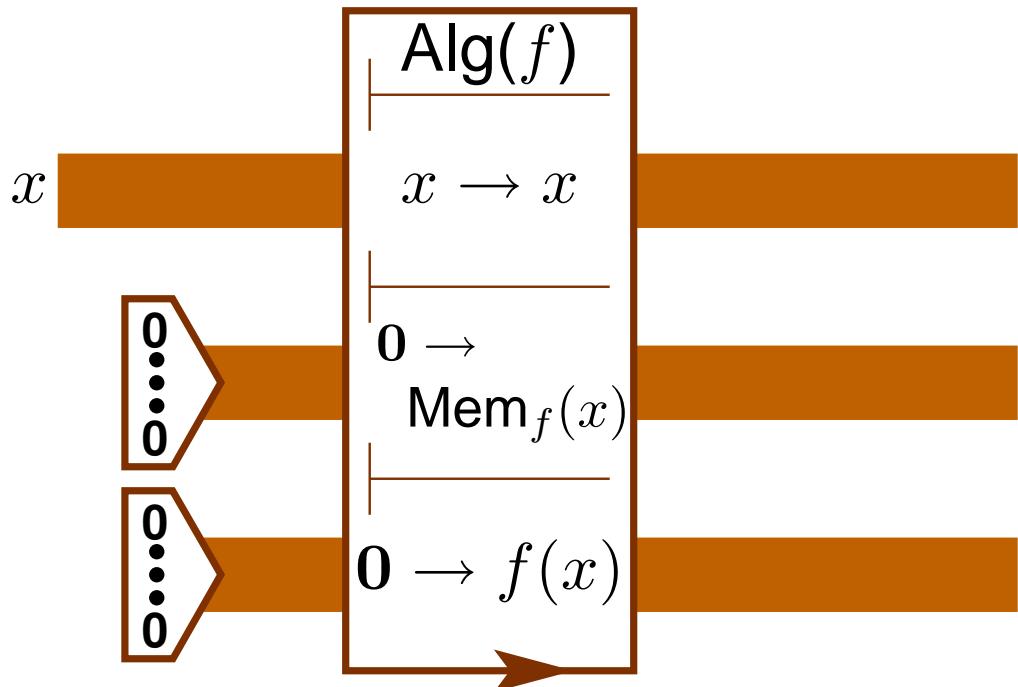


Reversifying the Comparison Circuit



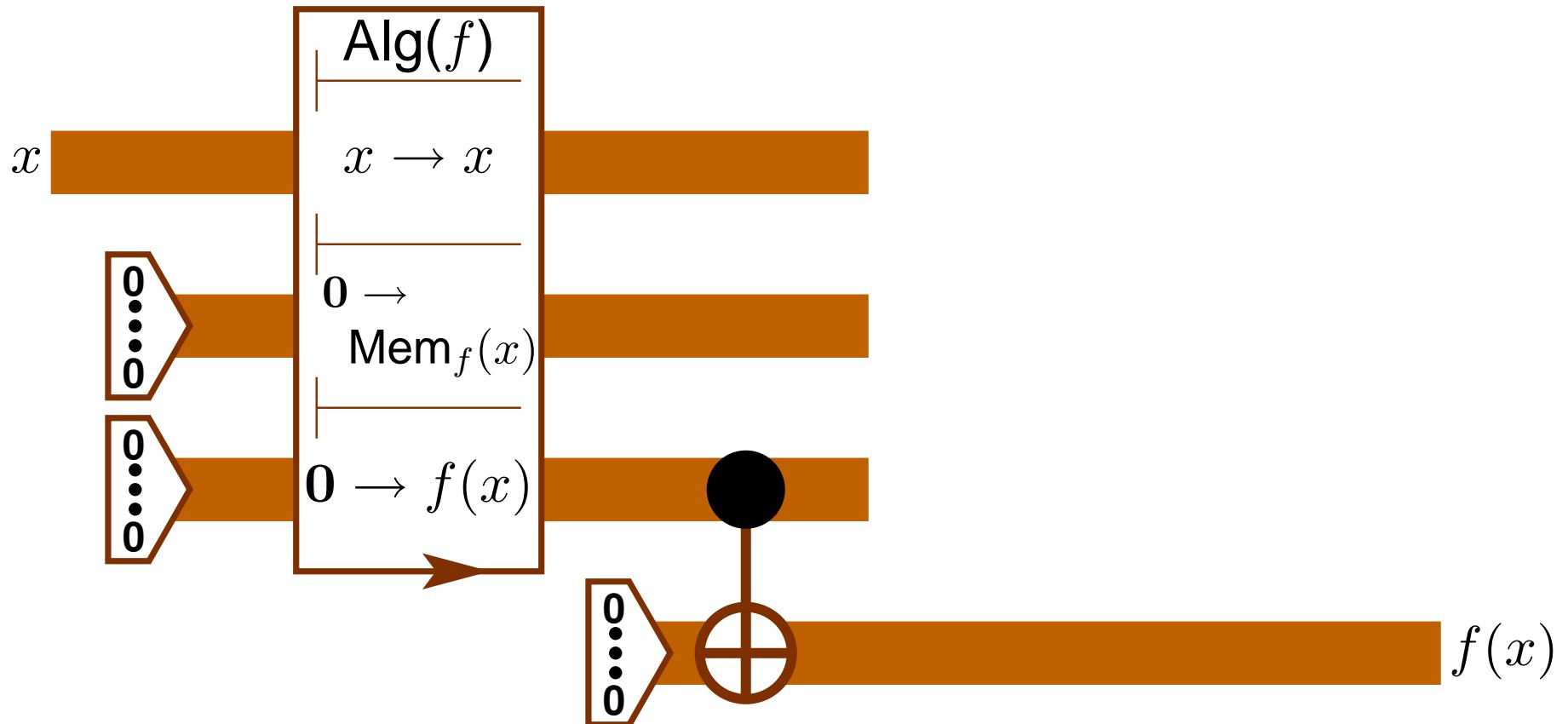
Erasing Memory

- Erasing memory by copying output and reversing.



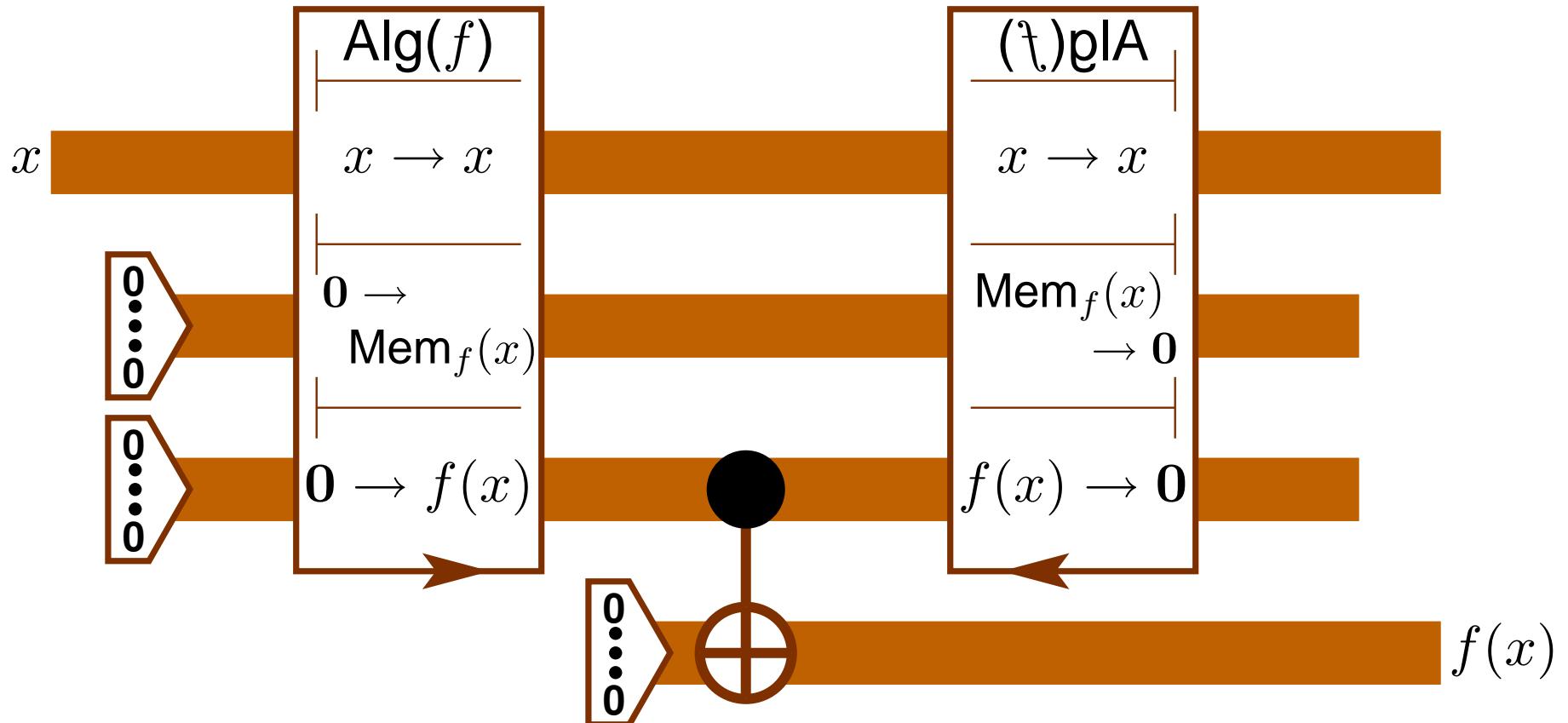
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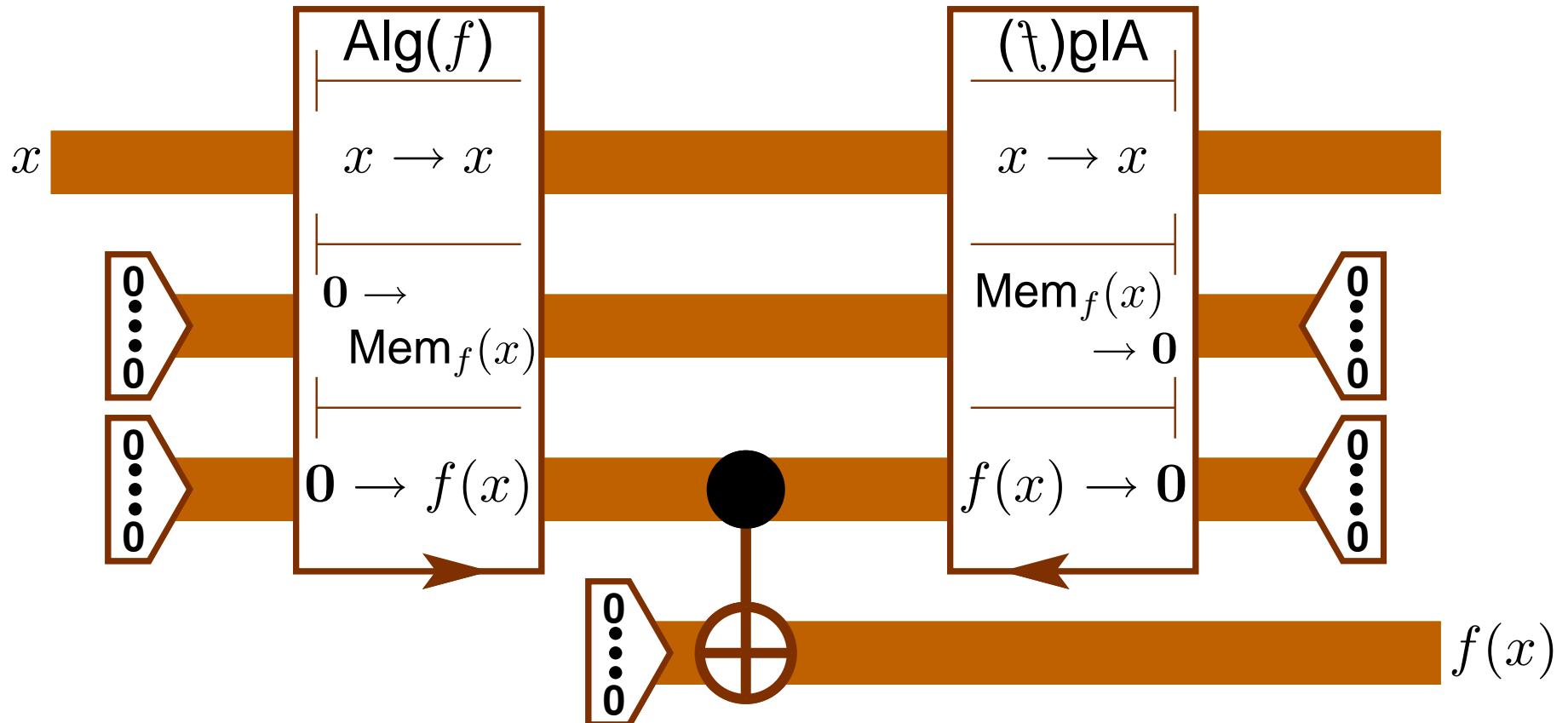
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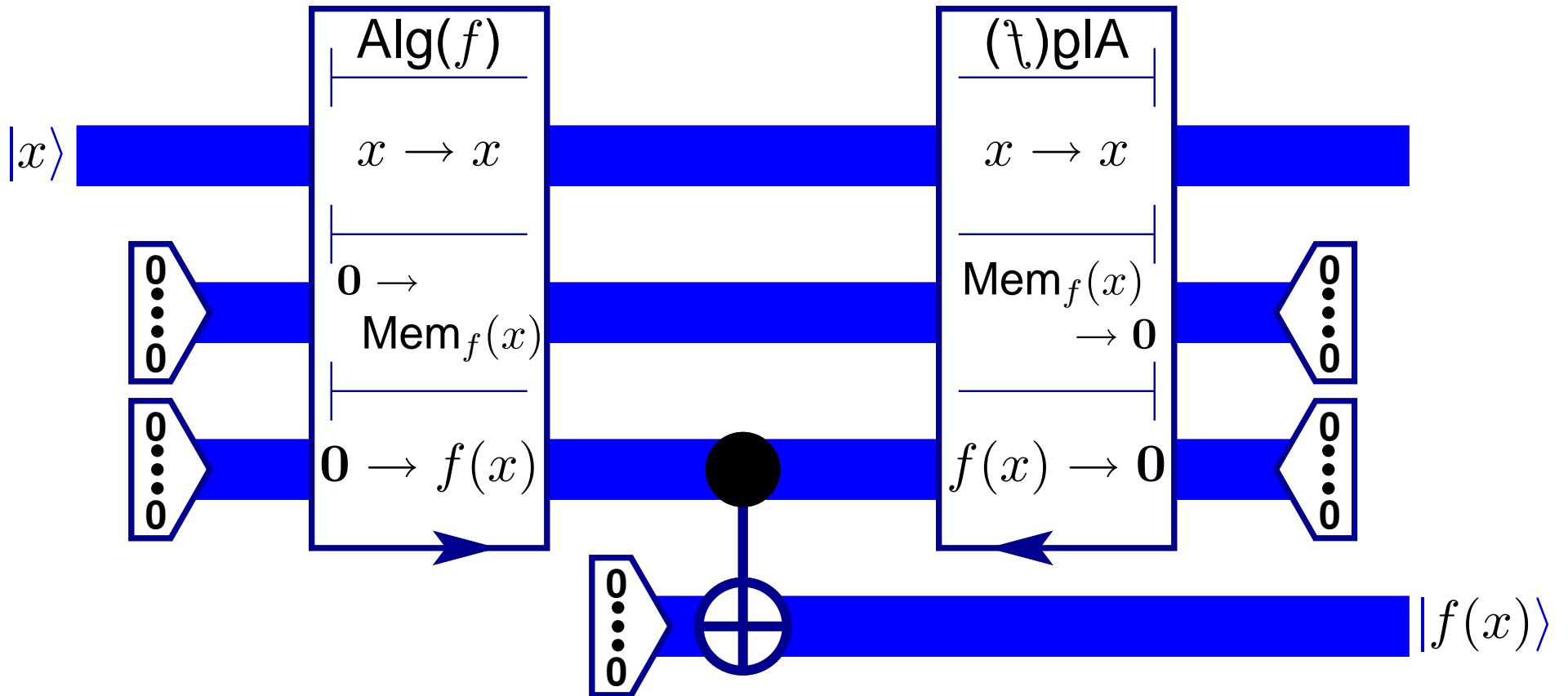
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Erasing Memory

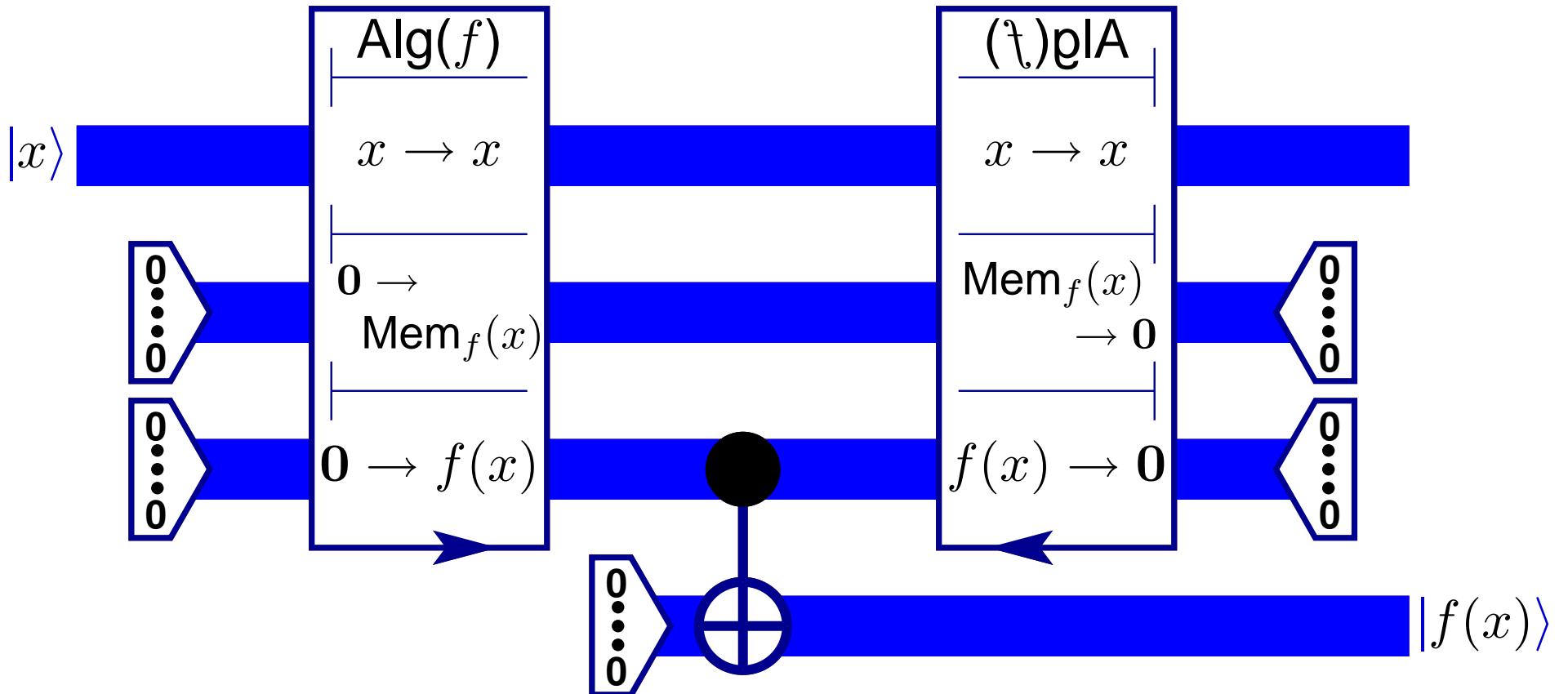
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- Classical reversible gates \rightarrow quantum gates.

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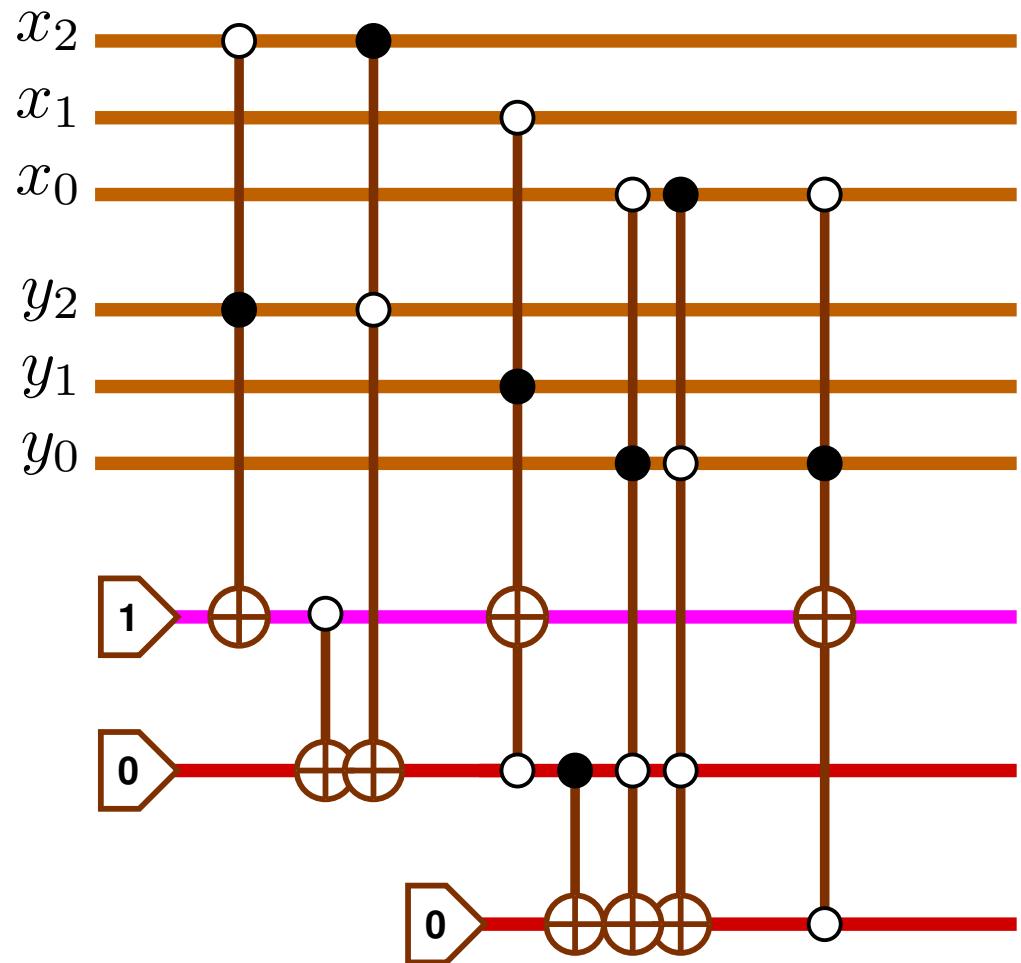


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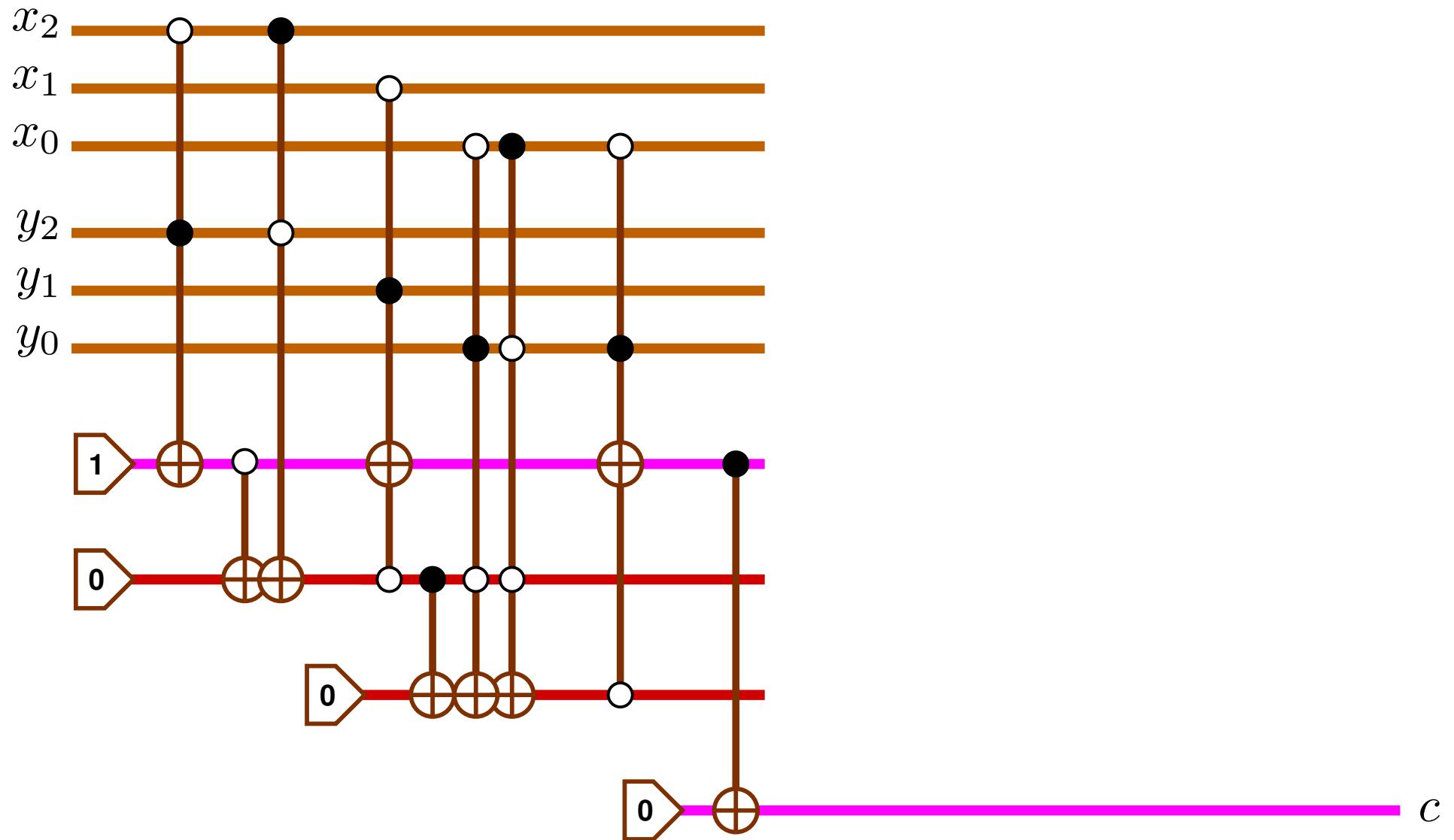
$$\sum_x \alpha_x |x\rangle_1 \rightarrow \sum_x \alpha_x |x\rangle_1 |f(x)\rangle_0$$



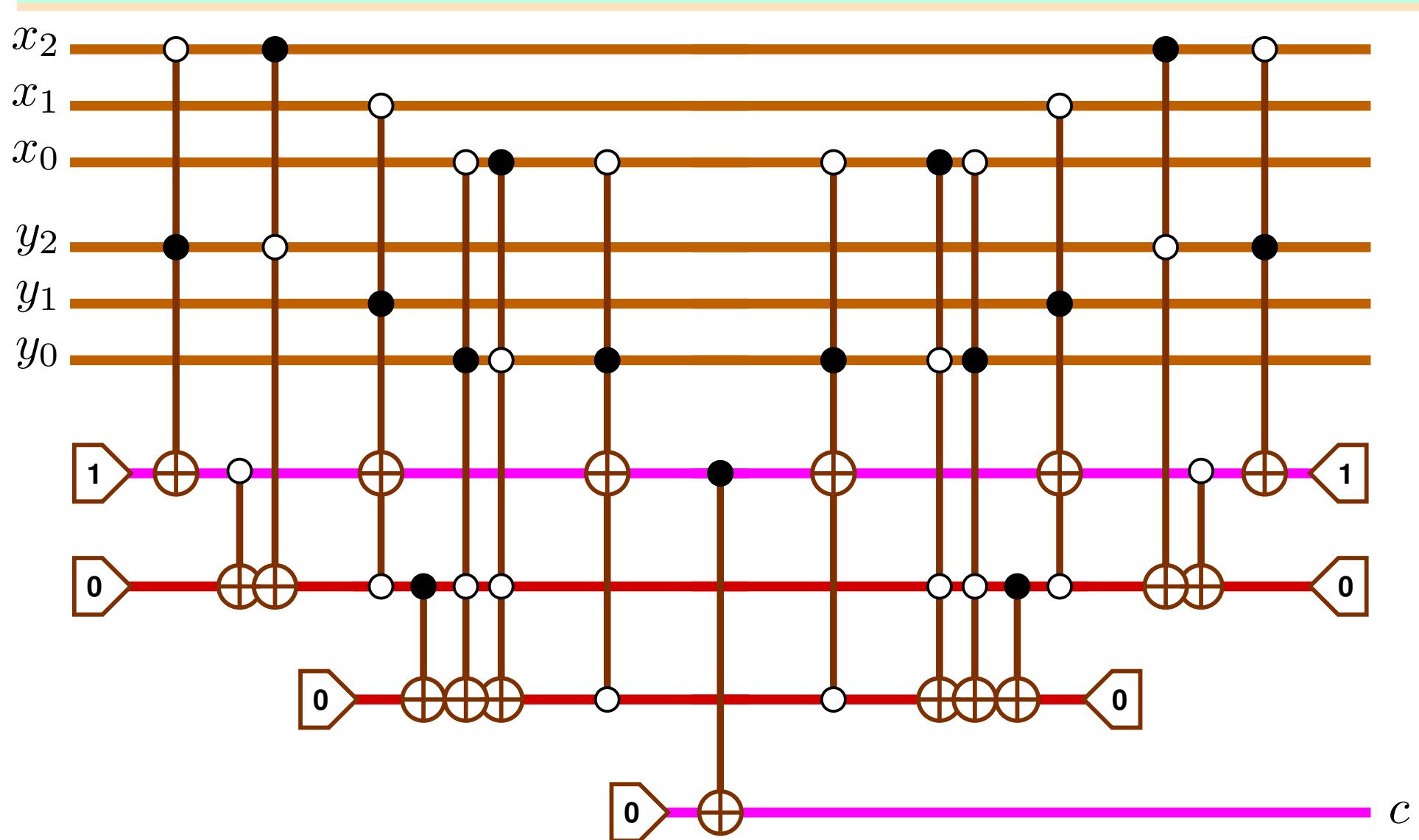
Coherent Comparison



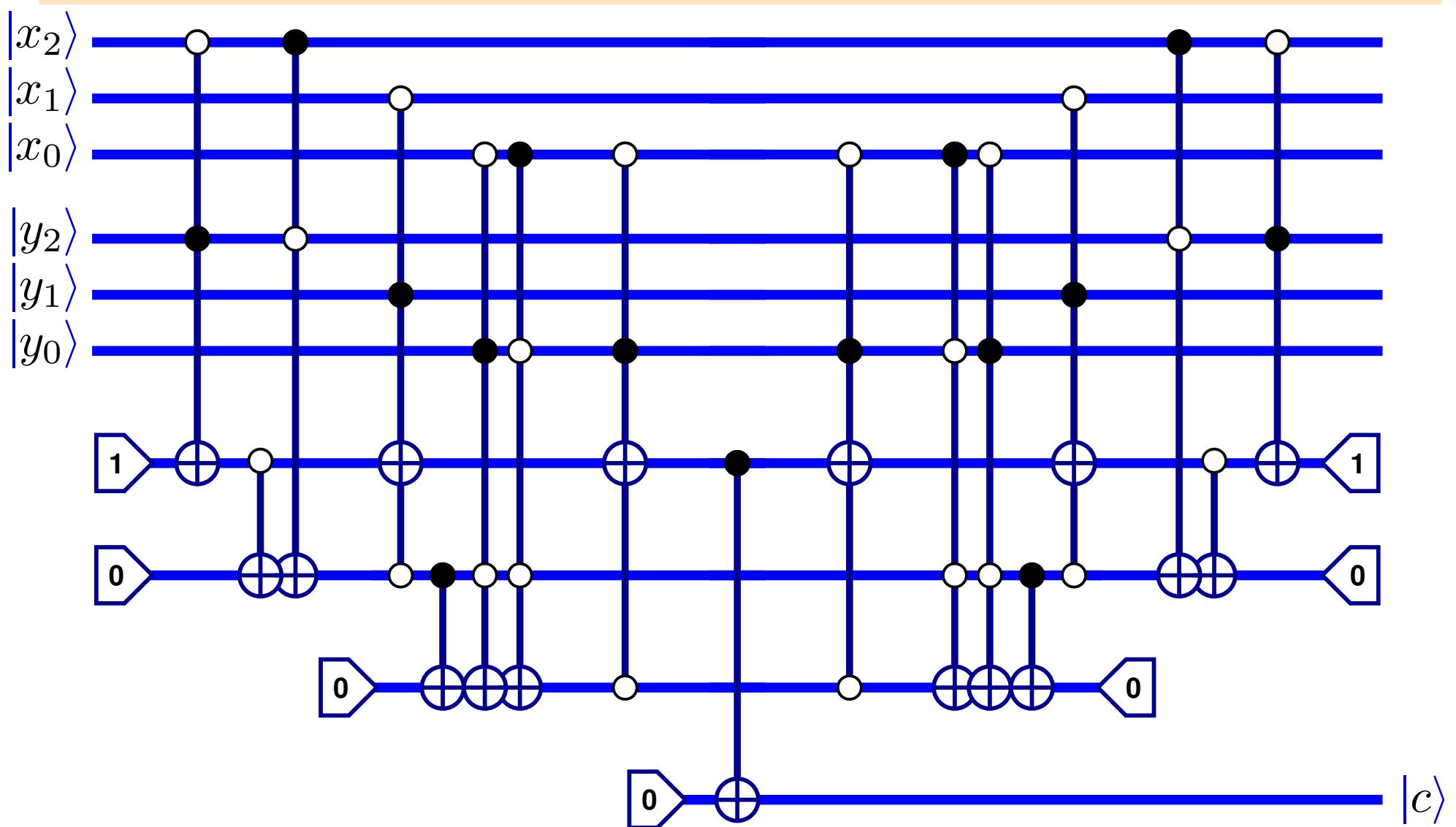
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Invertible Functions

- When can one coherently implement $\sum_x \alpha_x |x\rangle \rightarrow \sum_x \alpha_x |f(x)\rangle$?



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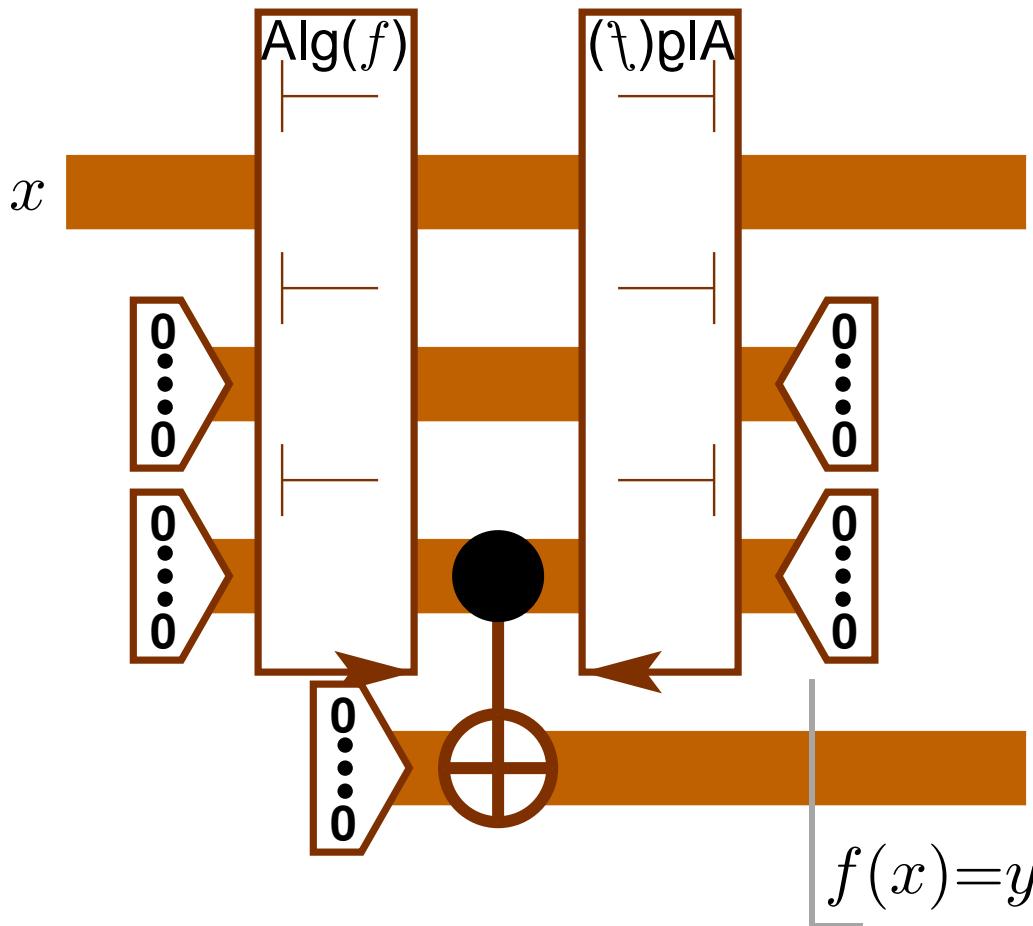
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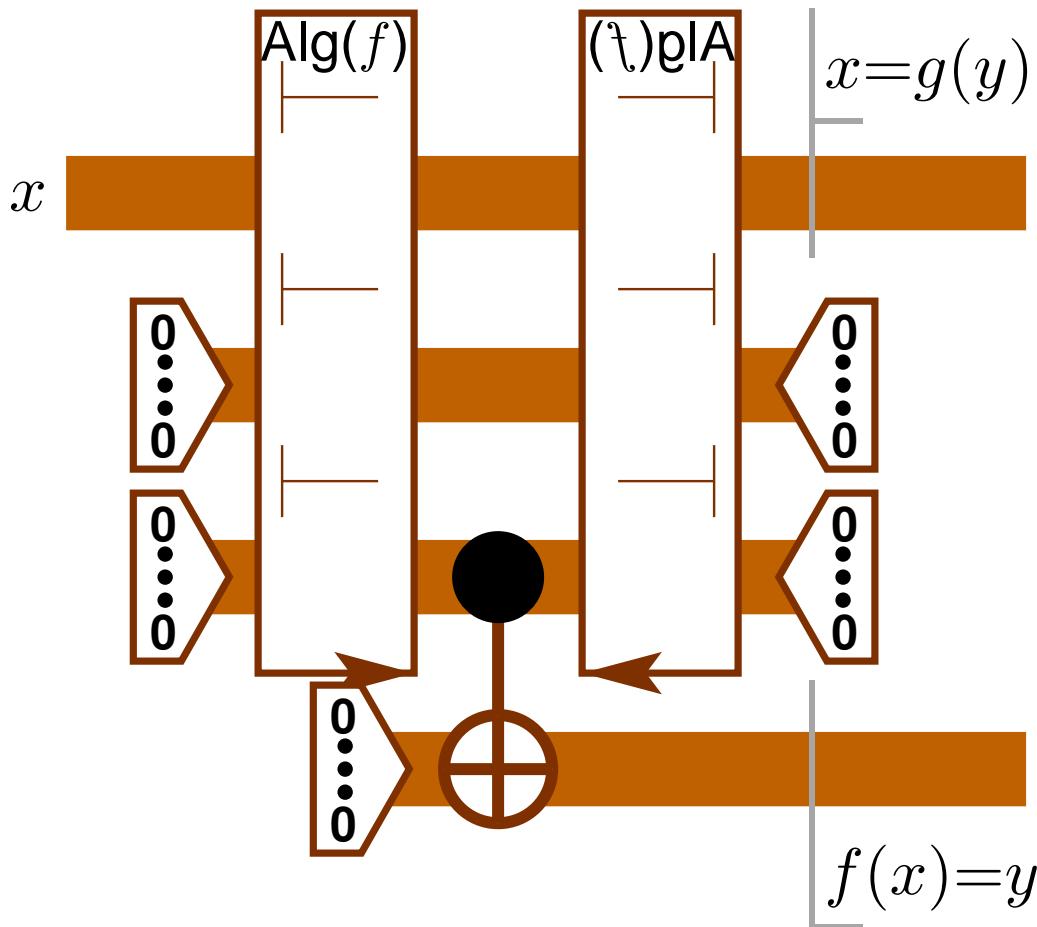
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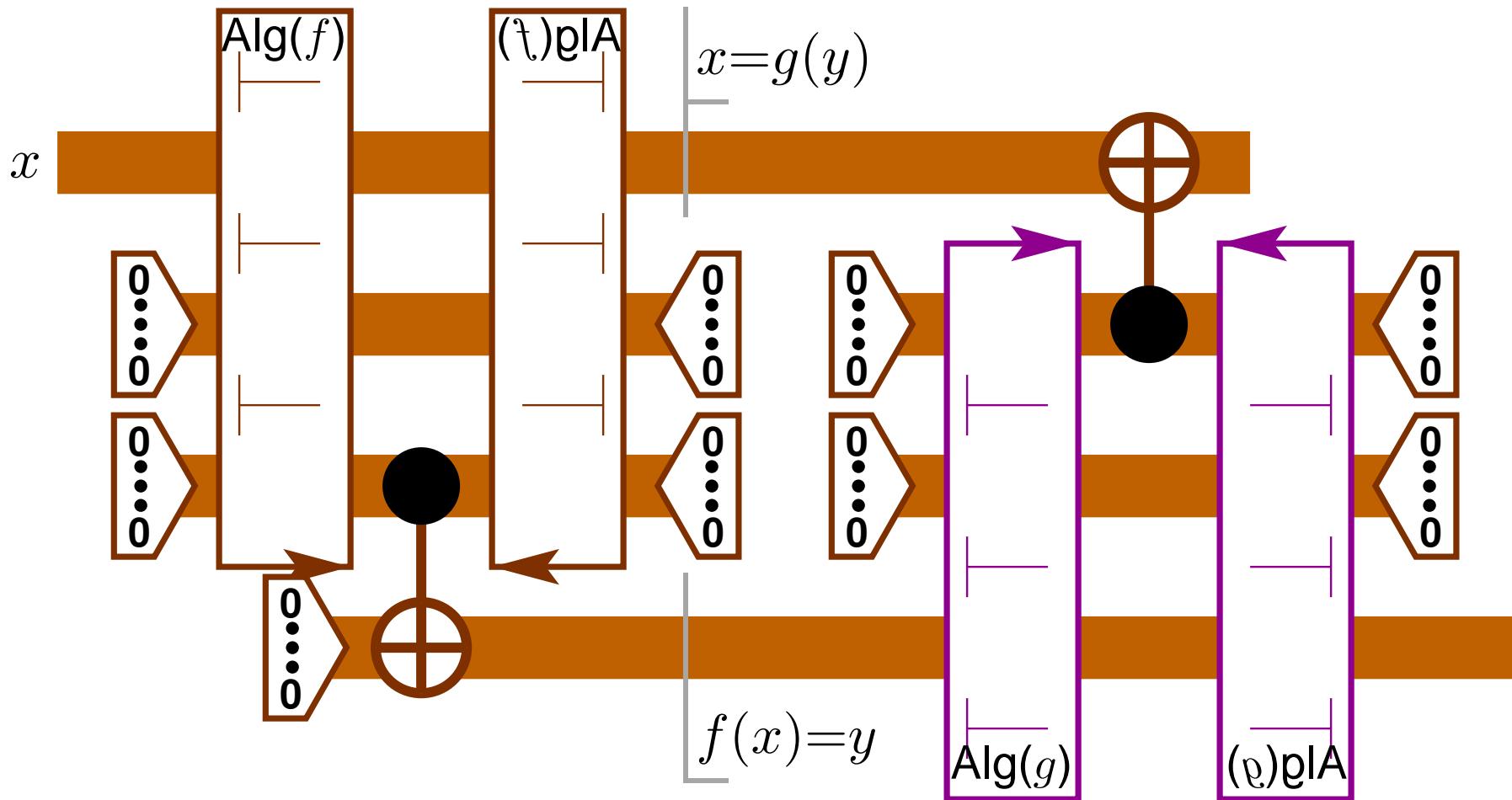
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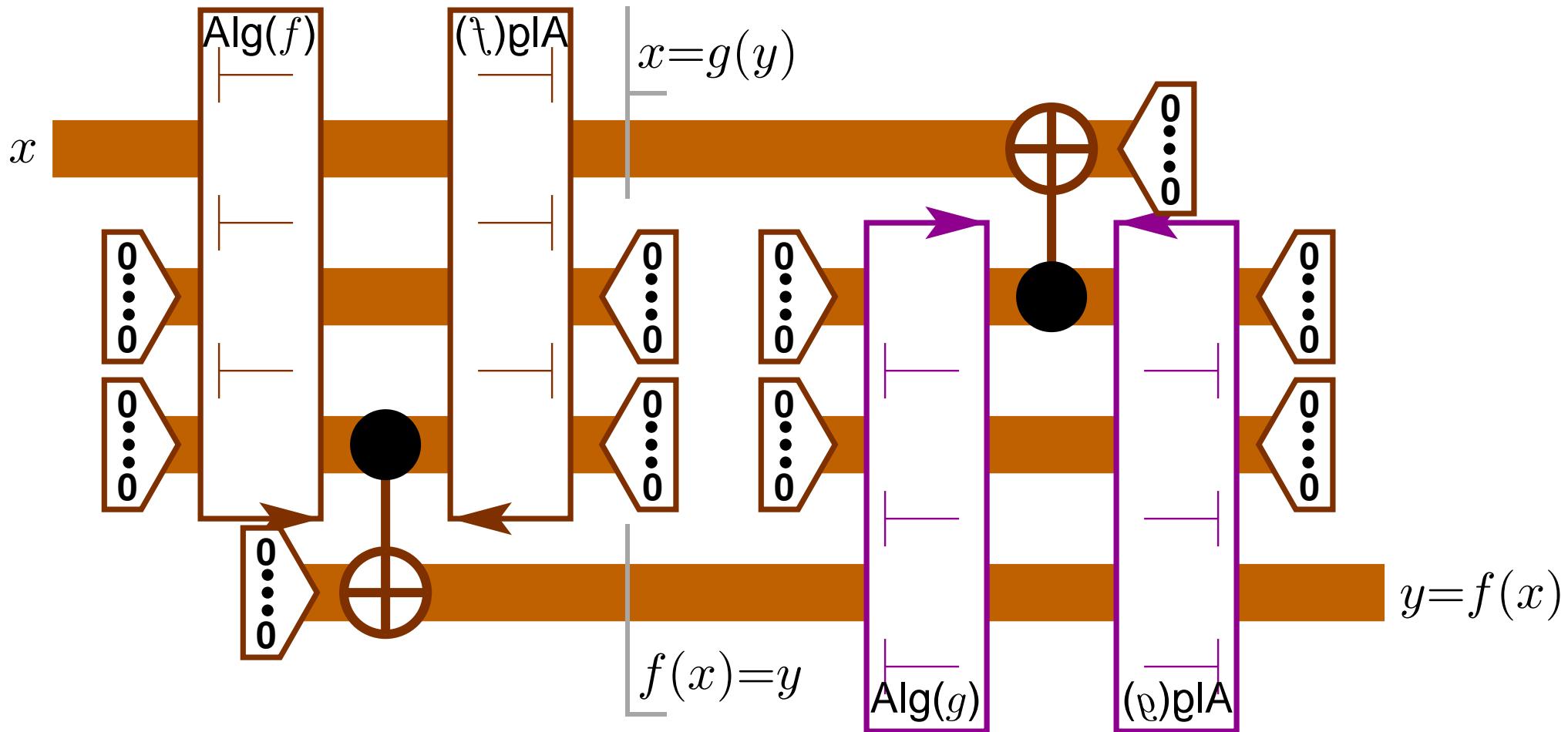
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Contents

Title: IQI 04, Seminar 7	0	top	8
Recognizing Patterns in Functions I	1	top	9
Recognizing Patterns in Functions II.....	2	top	10
Recognizing Patterns in Functions III	3	top	11
Example: Number Comparison	4	top	12
Classical Comparison Circuit	5	top	13
Converting to Reversible Logic I	6	top	14
Converting to Reversible Logic II	7	top	
Reversifying the Comparison Circuit		top	
Erasing Memory I		top	
Erasing Memory II		top	
Coherent Comparison		top	
Invertible Functions		top	
References		top	



References

- [1] C. H. Bennett, G. Brassard, S. Breidbart, and S. Wiesner. Quantum cryptography, or unforgeable subway tokens. In *Advances in Cryptology: Proceedings of Crypto'82*, pages 267–275. Plenum Press, 1982.
- [2] C. H. Bennett. Time/space trade-offs for reversible computation. *SIAM J. Comput.*, 18:766–776, 1989.

